

Compal confidential

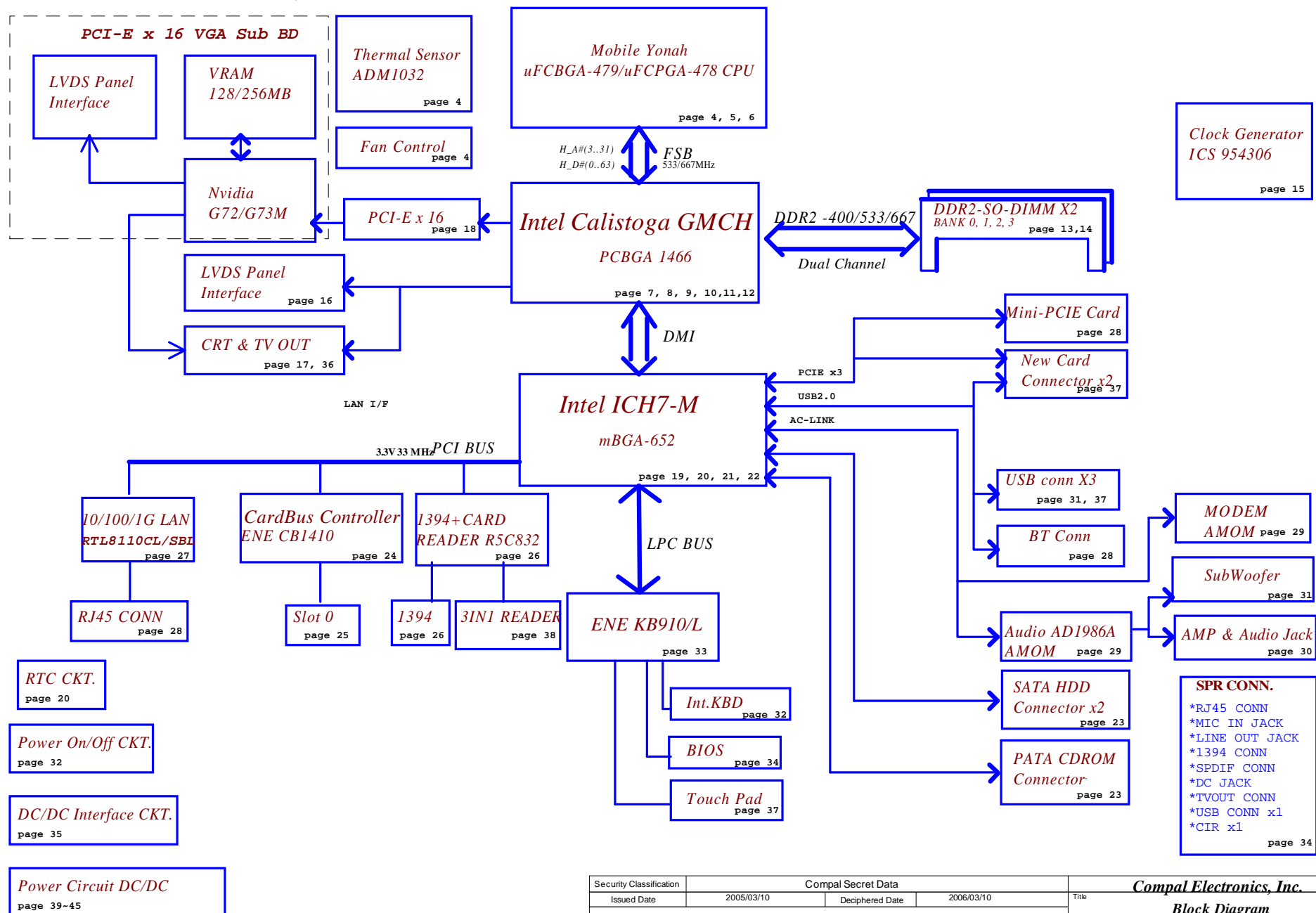
IGL50/51 Schematics Document

Mobile Yonah uFCPGA with Intel
Calistoga_GM/PM+ICH7-M core logic

2006-05-15

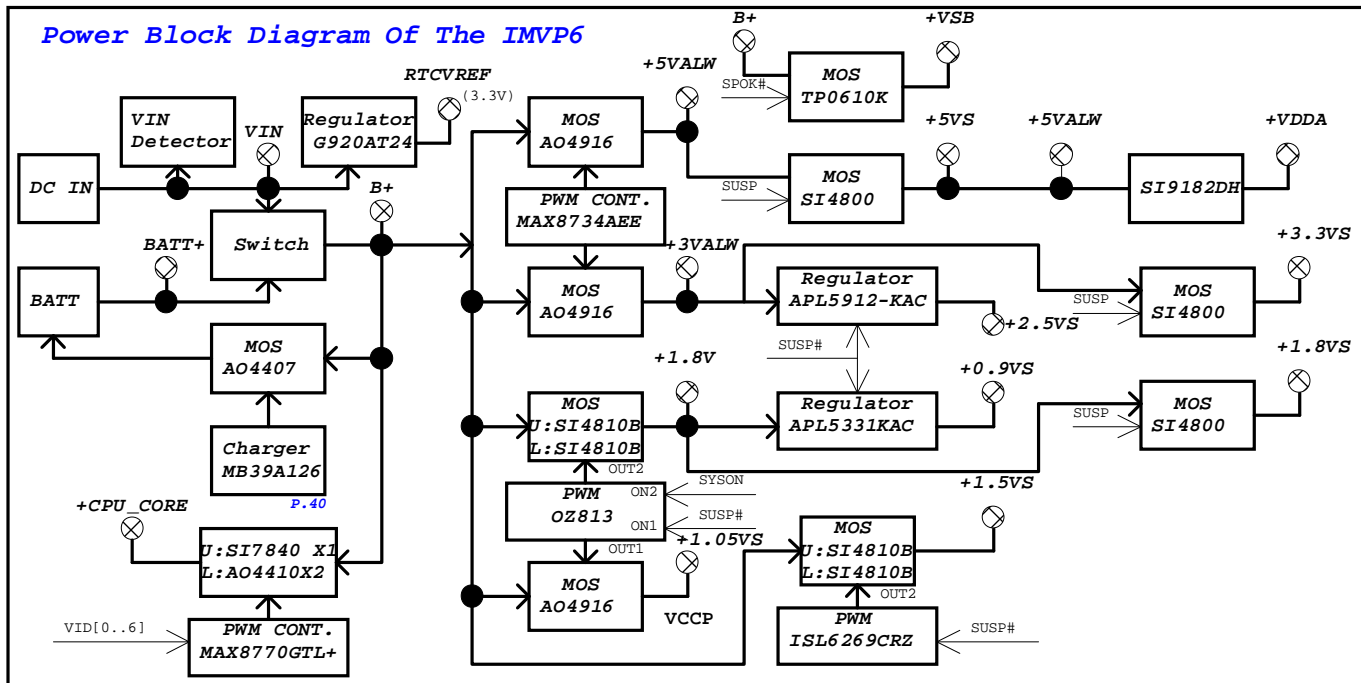
REV:0.1

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				Size	Document Number
				Custom	IGL50/51 LA-3771
Date: 星期五, 六月 08, 2006				Sheet	1 of 48
				Rev	0.1



Security Classification		Compal Secret Data		<p align="center">Compal Electronics, Inc.</p> <p align="center">Block Diagram</p>	
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Power Block Diagram Of The IMVP6



PCI DEVICES

EXTERNAL	IDSEL#	REQ/GNT#	PIRQ
CARD BUS CB1410	AD20	2	PCI_PIRQA#
CARD READER & 1394 R5C832	AD22	0	PCI_PIRQG# PCI_PIRQH#
LAN CONTROLLER RTL8110SBL/CL	AD17	3	PCI_PIRQF#

PCIE LANE

LANE	DEVICE
1	Express Card
2	Mini Card

I2C / SMB Address

DEVICE	ADDRESS R/W
KB910/L (SM1-Pulled-Up 5V)	
AT24C16AN	A3/A2 H
SMART BATTERY	17/16 H
KB910/L (SM2-Pulled-Up 3.3V)	
ADM1032AR	99/98 H
G7xM (I2CC-Pulled-Up 3.3V)	
G781-1 (RESERVED)	9B/9A
ICH7M SM Bus	
ICS9LPR325AKLFT	D3/D2 H (3.3V)
DDR II DIMM0	A1/A0 H (3.3V)
DDR II DIMM1	A3/A2 H (3.3V)
Express Card	NC (2.5V)
Mini-Express	NC (2.5V)

USB

PORT	DEVICE
0	LEFT SIDE
1	BLUE TOOTH
2	RIGHT SIDE
3	JP810
4	RIGHT SIDE
5	CMOS
6	RIGHT SIDE

BOM Structure

MARK	FUNCTION
@	NC FOR ALL
EXP@	PCIE-NEW CARD
BT@	BLUE TOOTH
UMA@	Internal 945GM
VGA@	External G7xM
SUBWOOFER@	SUBWOOFER
HGT30@	HGT30
CB@	PCMCIA/CARD BUS
GIGA@	8110SBL (SCL) Giga LAN
10/100@	8110CL 10/100Mb LAN

Voltage Rails

power plane	+B LDO3 LDO5	+5VALW +3VALW	+1.8V +5V	+5VS +3VS +2.5VS +1.8VS +1.5VS +VGA_CORE +1.2VS +0.9VS +CPU_CORE +VCCP
State S4 : STD S5 : SOFT OFF				
S0	O	O	O	O
S1	O	O	O	O
S3 : STR	O	O	O	X
S5 S4/AC	O	O	X	X
S5 S4/ Battery only	O	X	X	X
S5 S4/AC & Battery don't exist	X	X	X	X

MB_ID

MB ID	P NAME
0	IGL-50
1	IGL-51

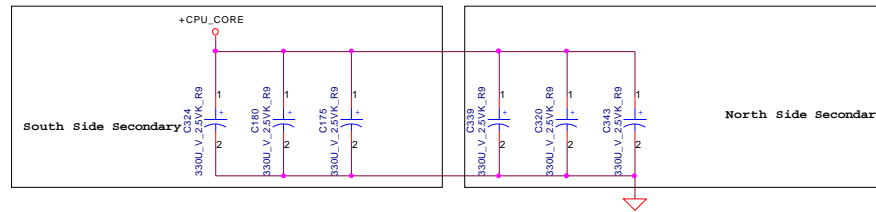
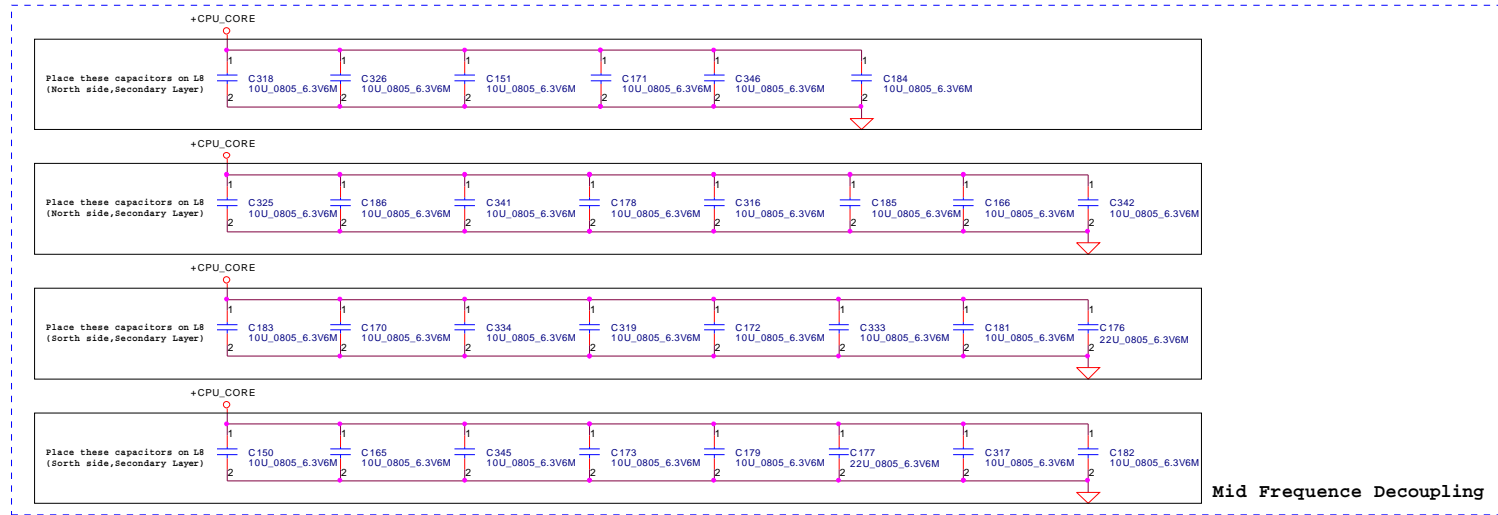
BRD_ID

R119(Ra)=100K Ohm

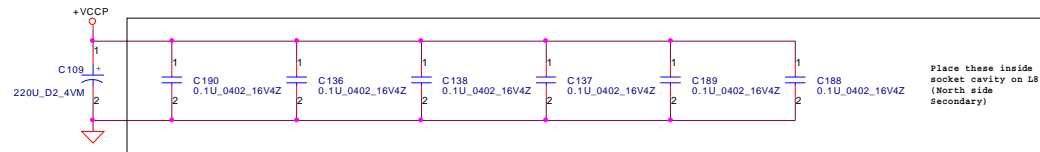
ID	MB REV#	R115(Rb)	Vab
0	R0.1 (EVT)	0	0V
1	R0.2 (DVT)	8.2K	0.25V
2	R0.3 (PVT)	18K	0.50V
3	R1.0 (MP)	33K	0.82V
4		56K	1.19V
5		100K	1.65V
6		200K	2.20V
7		NC	3.30V

+3VALW	KB910L SB RTL8110SBL/CL	mA 160mA
+CPU_CORE	CPU	36A
+VCCP	CPU NB	2.5A 9.8A (14.7A)
+5VS	EXPRESS CARD HDD ODD MDC APA2066 TPA0211 AD1986 USB PORT * 6	1A 1.5A 1.8A 300mA 1A mA 70mA 3A
+3VS	NB EXPRESS CARD CLK_GEN LCDVCC VGA_CARD (G7XM) SB R5C832 BIOS ROM KB910L CB1410	480mA 1A 200mA 1A 655mA 680mA mA 15mA 200mA mA
+2.5VS	VGA_CARD (G7XM) NB	130mA (143mA)
+1.8V	DDR2_DIMM NB (667Mhz)	8A 3.1A
+1.8VS	GDDR2 VGA_CARD (G7XM)	6A 4.06A
+0.9VREF	DDR2_DIMM	10mA
+0.9VS	GDDR2 DDR2_DIMM	1A 2A
+1.5V	SB	40mA
+1.5VS	NB SB MiniCard EXPRESS CARD VGA_CARD (G7XM)	8.9A(13.8A) 3.8A 1A 0.65A 2A

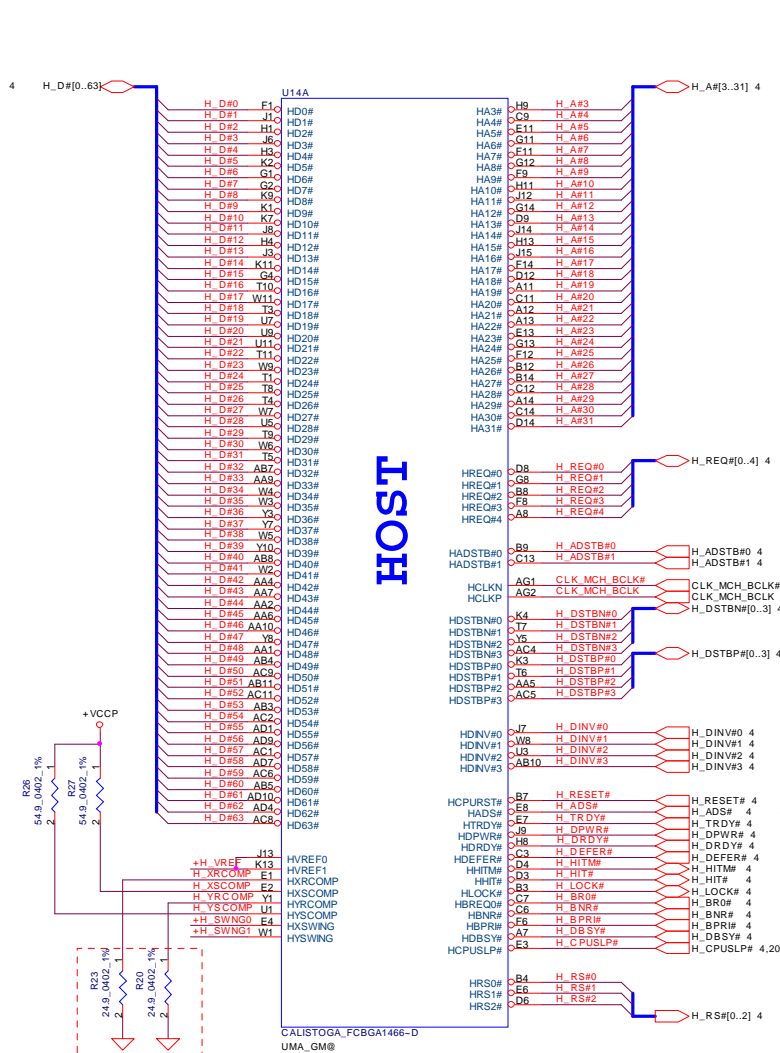
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				Rev 0.1
				Date: 星期五, 六月 08, 2006
				Sheet 3 of 48



ESR <= 1.5m ohm
Capacitor > 1980uF

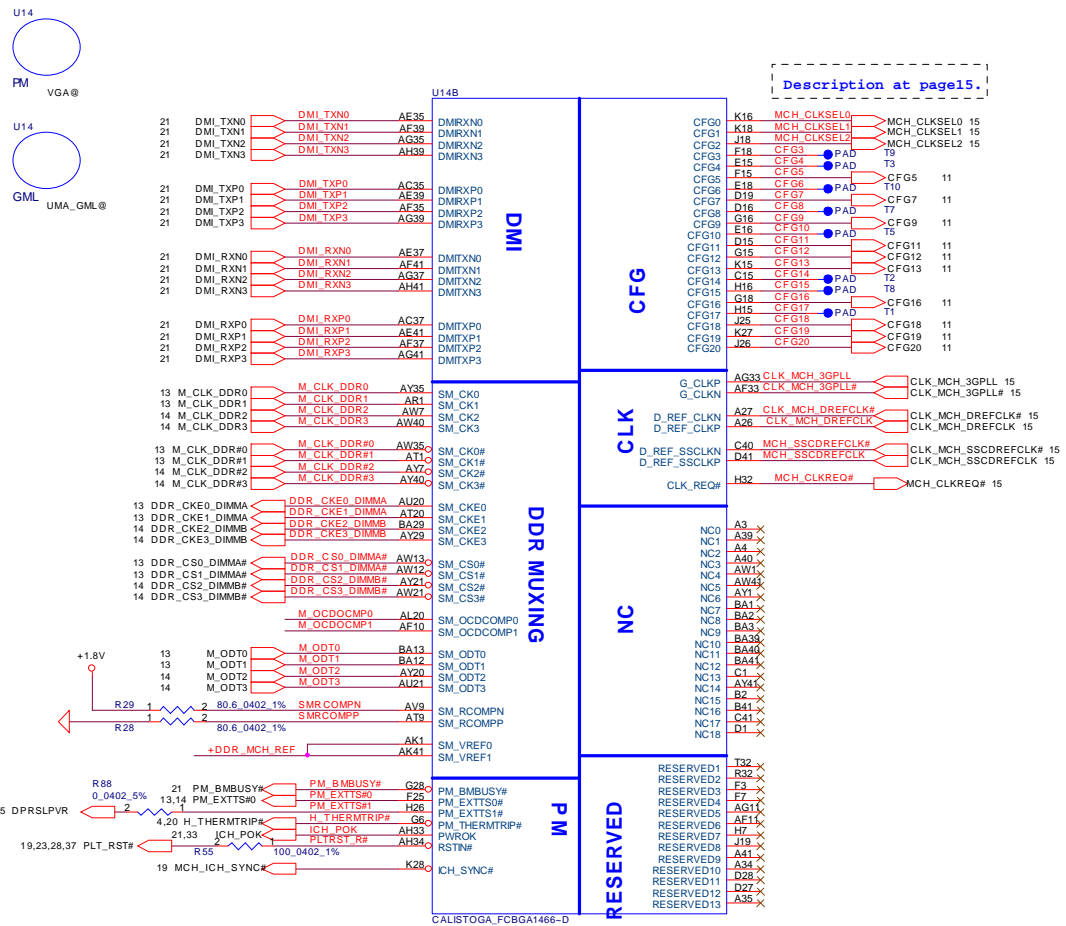
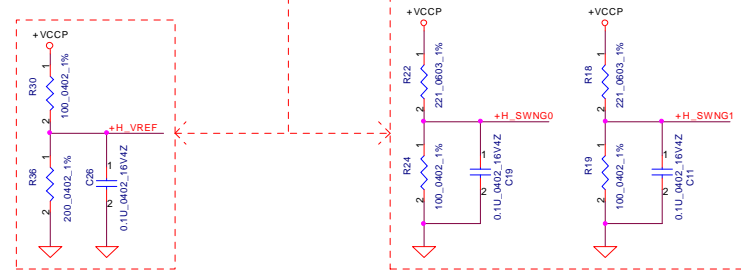


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				Rev 0.1
				Date: 星期四, 六月 08, 2006 Sheet 6 of 48



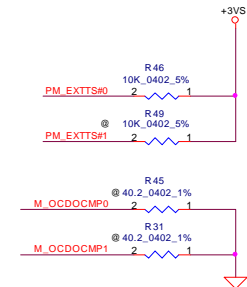
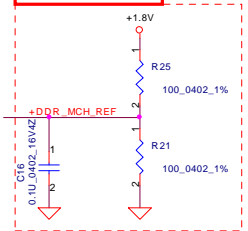
HOST

Layout Note:
H_XRCOMP / H_YRCOMP / H_VREF / H_SWNG0 / H_SWNG1 trace width and spacing is 10/20.



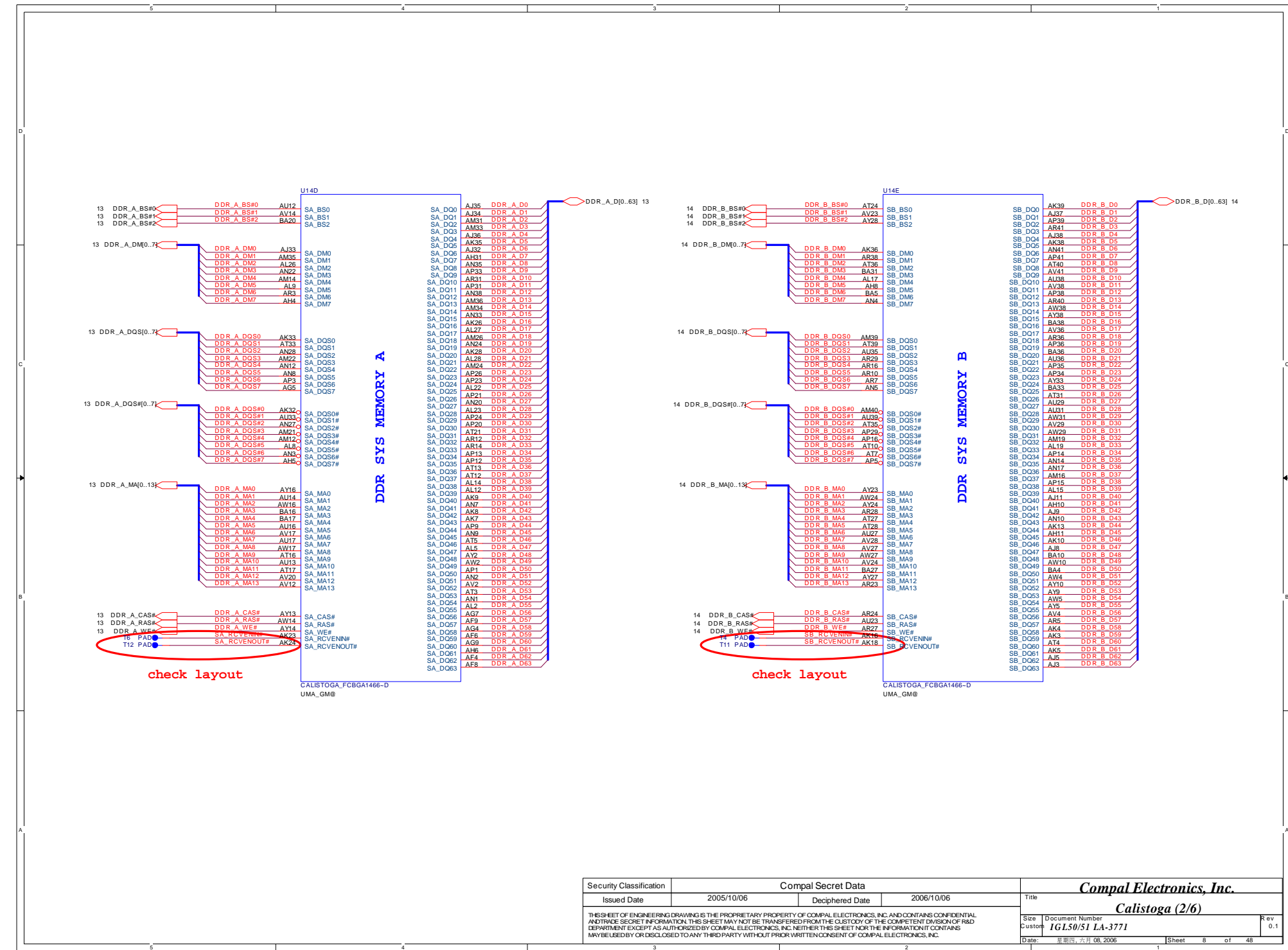
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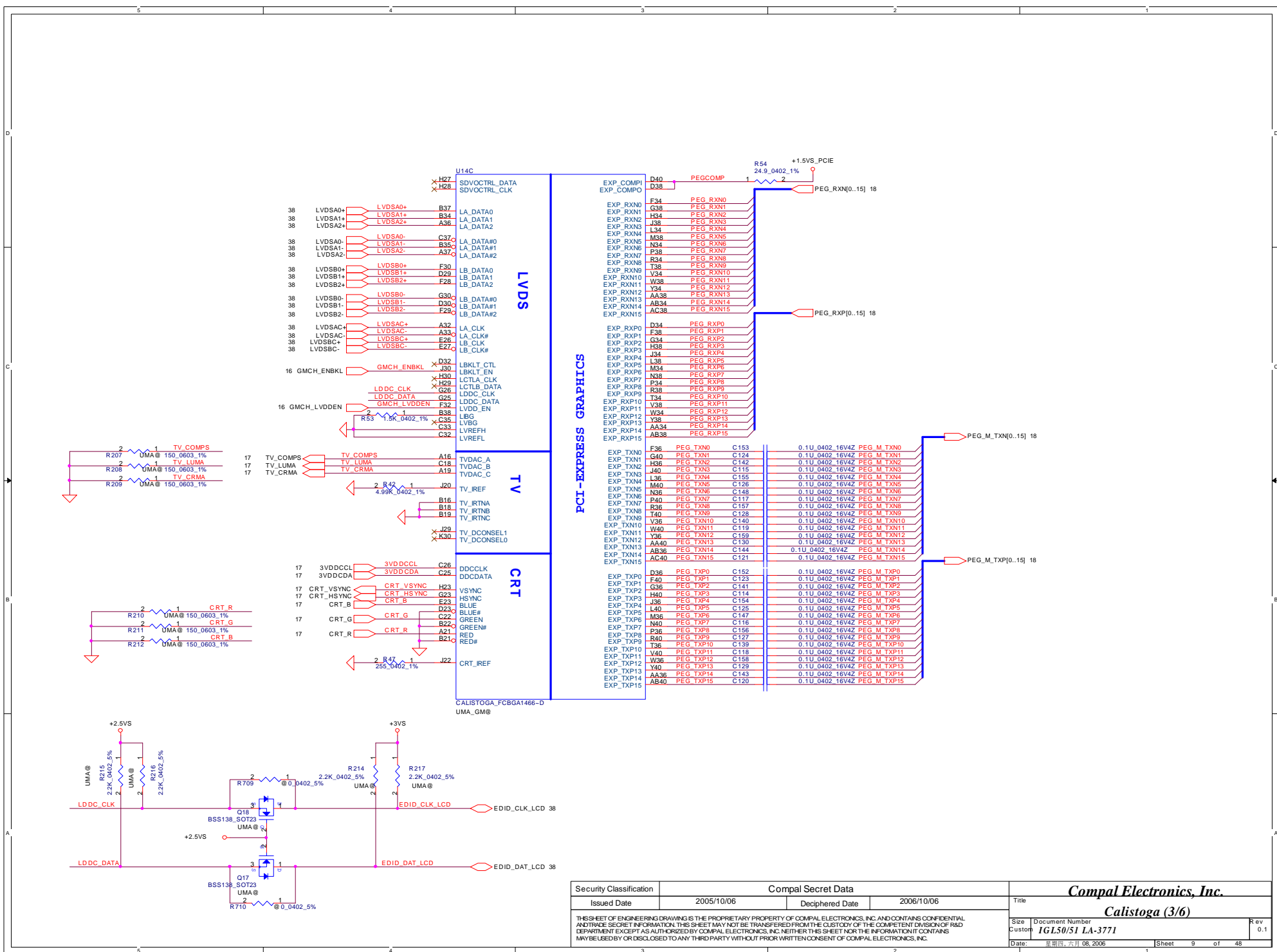


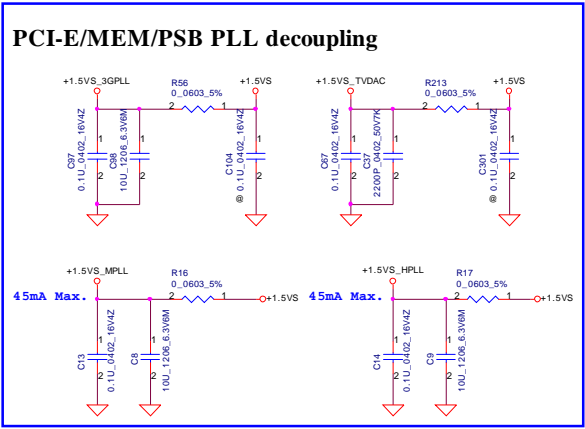
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Date:		星期日, 六月 08, 2006		Sheet 7 of 48	

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Calistoga (1/6)



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				Date	星期五, 六月 08, 2006
				Sheet	8 of 48
				Rev	0.1



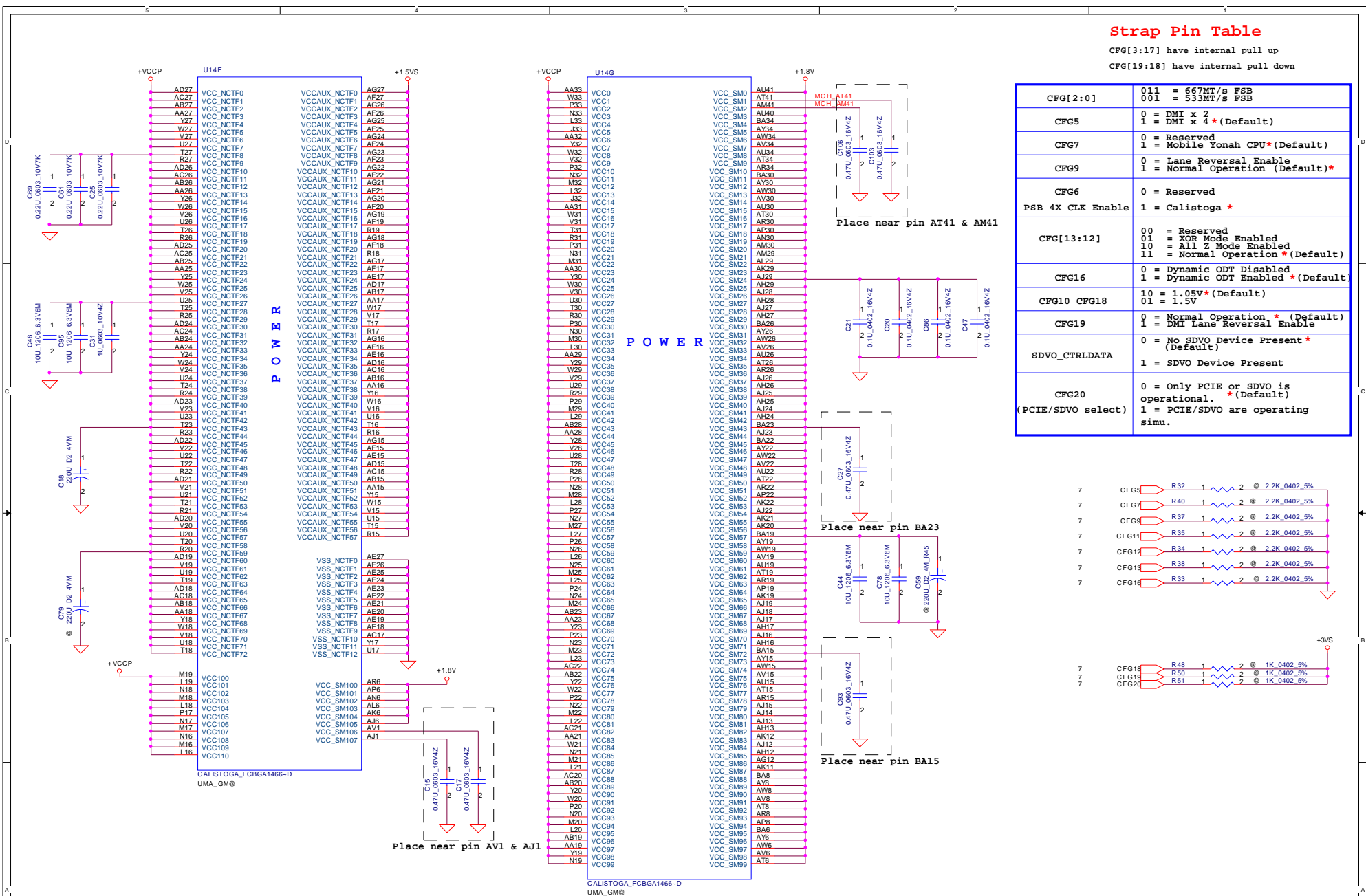
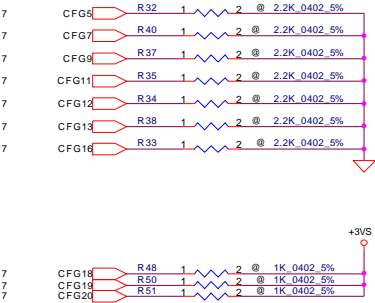


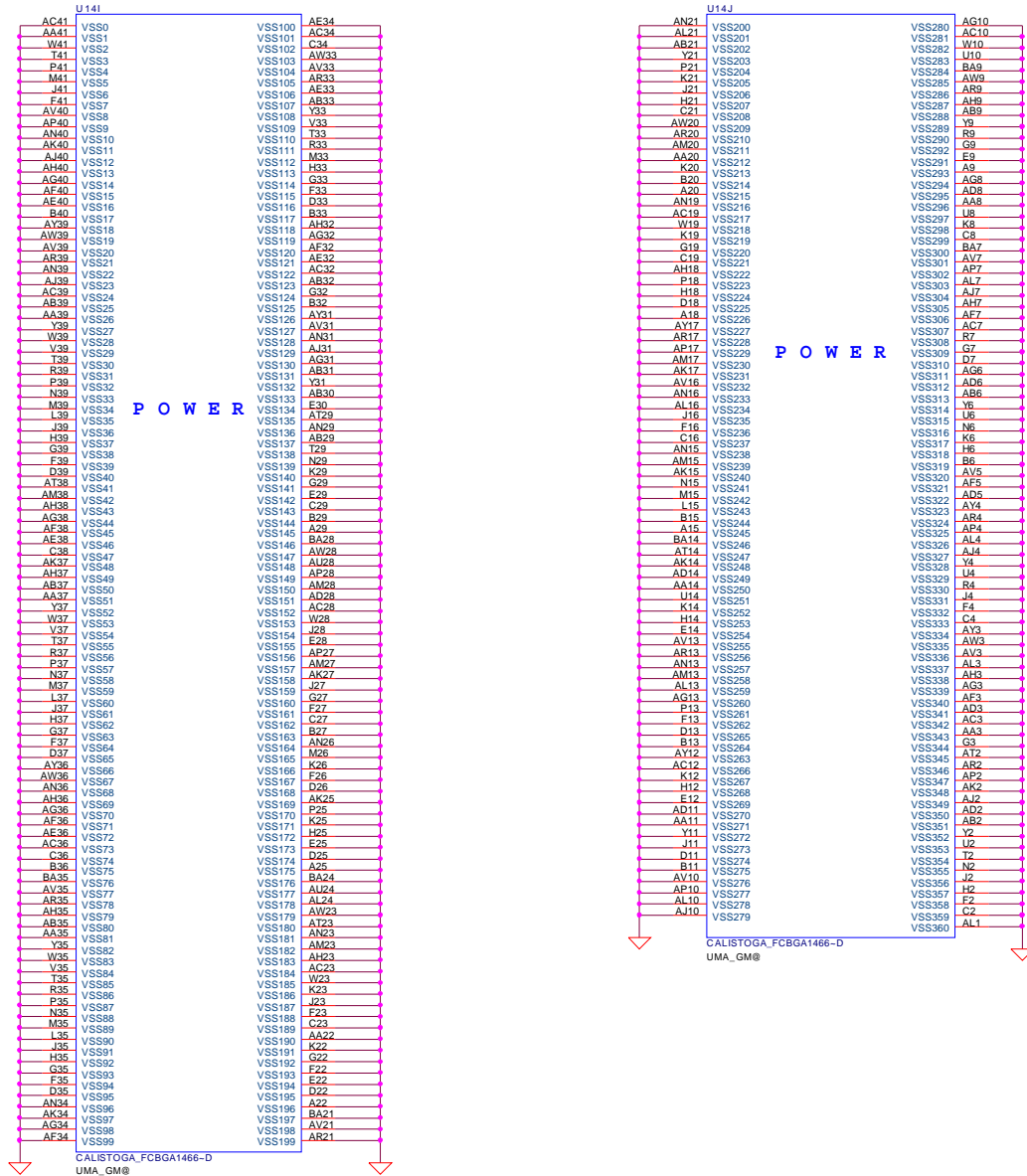
WWW.AliSaler.Com

Strap Pin Table

CFG[3:17] have internal pull up
CFG[19:18] have internal pull down

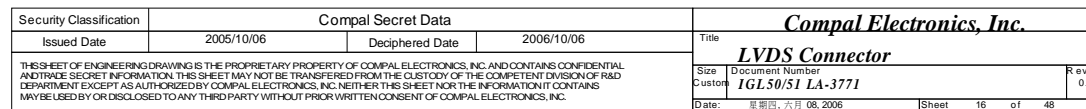
CFG[2:0]	011 = 667MT/s FSB 001 = 533MT/s FSB
CFG5	0 = DMI x 2 1 = DMI x 4 *(Default)
CFG7	0 = Reserved 1 = Mobile Yonah CPU *(Default)
CFG9	0 = Lane Reversal Enable 1 = Normal Operation (Default)*
CFG6	0 = Reserved PSB 4X CLK Enable 1 = Calistoga *
CFG[13:12]	00 = Reserved 01 = XOR Mode Enabled 10 = Mobile Yonah CPU *(Default) 11 = Normal Operation *(Default)
CFG16	0 = Dynamic ODT Disabled 1 = Dynamic ODT Enabled *(Default)
CFG10 CFG18	10 = 1.05V *(Default) 01 = 1.5V
CFG19	0 = No SDVO Device Present *(Default) 1 = DMI Lane Reversal Enable
SDVO_CTRLDATA	0 = No SDVO Device Present *(Default) 1 = SDVO Device Present
CFG20 (PCIE/SDVO select)	0 = Only PCIE or SDVO is operational. *(Default) 1 = PCIE/SDVO are operating simul.

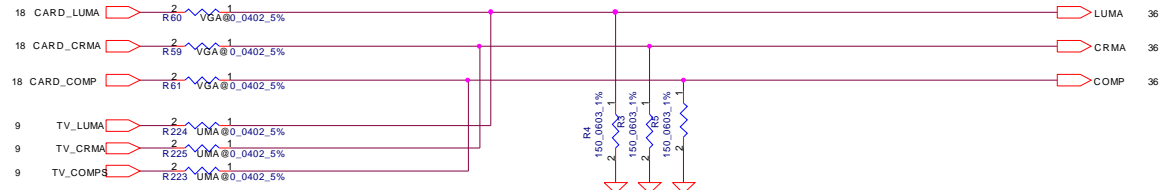




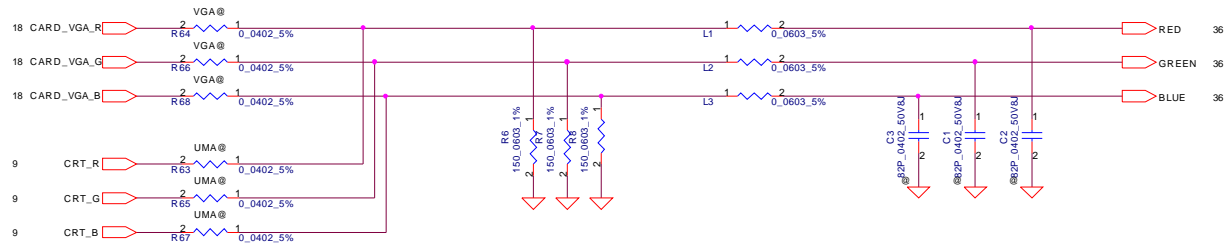
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				Rev 0.1	
				Date: 星期四, 六月 08, 2006	
				Sheet 12 of 48	



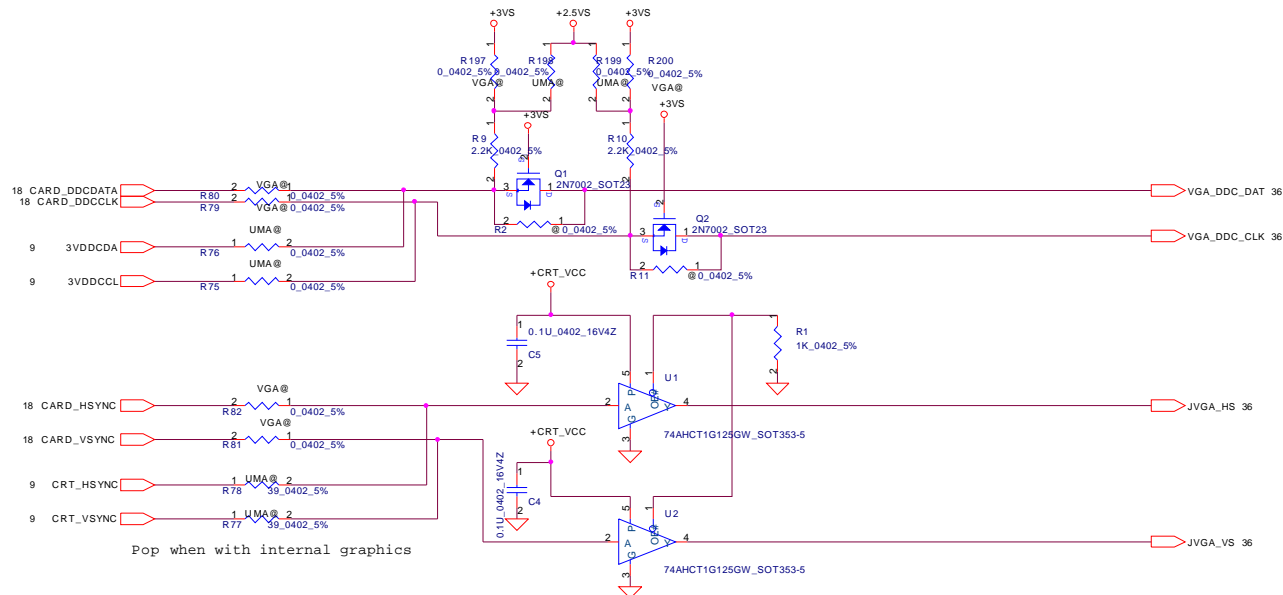


TV-OUT Conn.

Pop when with internal graphics

CRT Conn.

Pop when with internal graphics

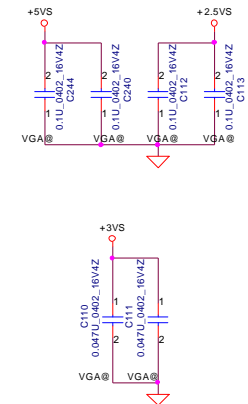
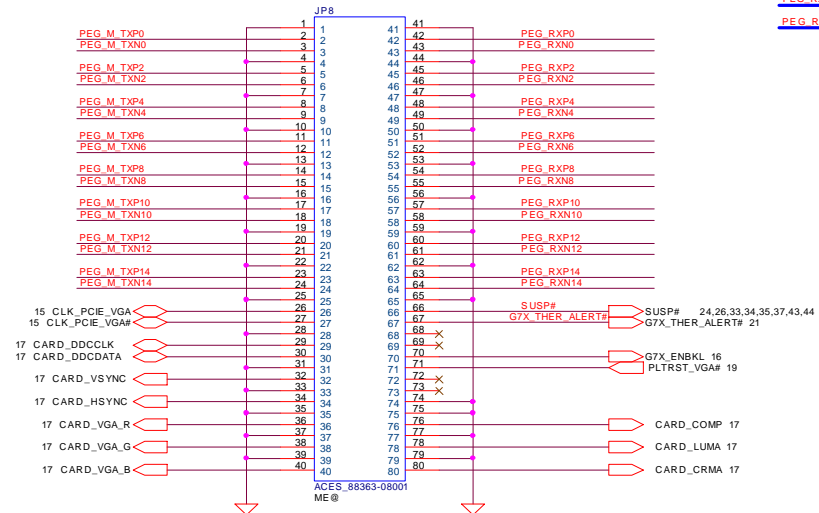
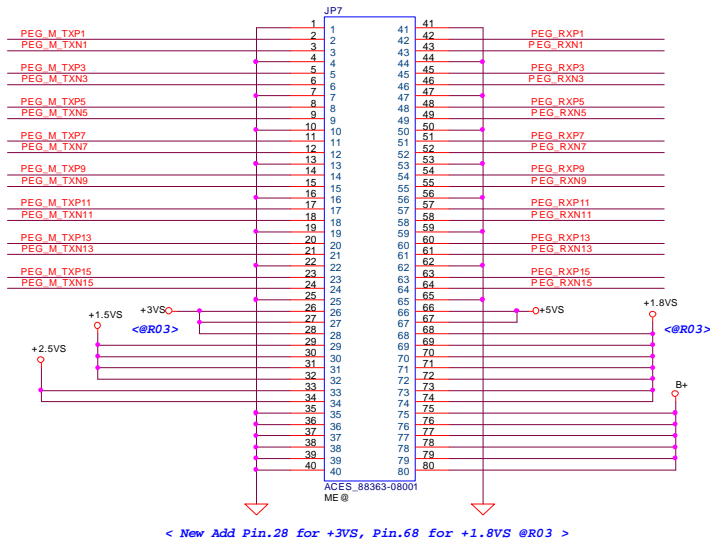


Pop when with internal graphics

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Size	Document Number			IGL50/S1 LA-3771	
Custom				Date:	星期四, 六月 08, 2006
				Sheet	17 of 48

MAX. 4.06A @ 1.8V
MAX. 130mA @ 2.5V
MAX. 655mA @ 3.3V

PEG_M_TXP[0..15] PEG_M_TXP[0..15] 9
PEG_M_TXN[0..15] PEG_M_TXN[0..15] 9
PEG_RXP[0..15] PEG_RXP[0..15] 9
PEG_RXN[0..15] PEG_RXN[0..15] 9

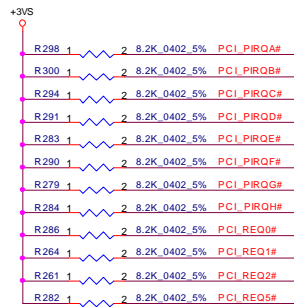
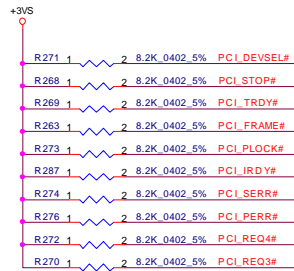


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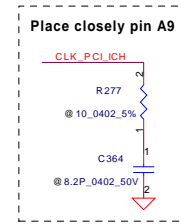
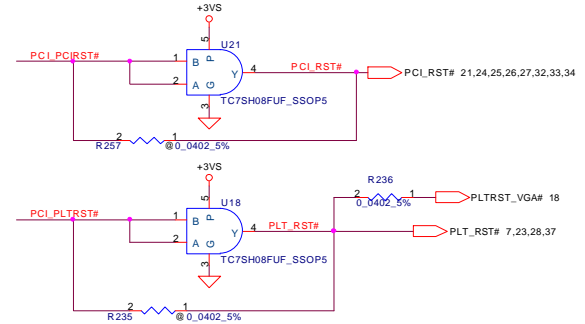
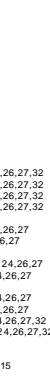
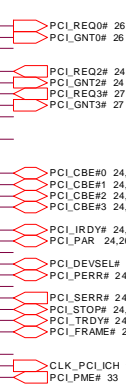
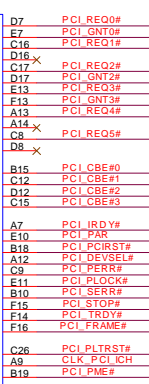
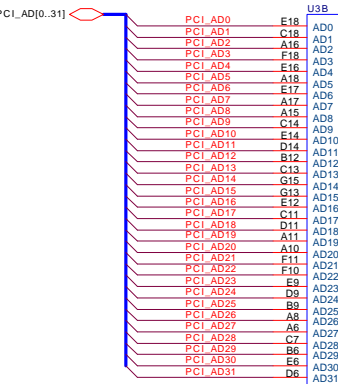
VGA/B connector

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Size	1GL50/S1 LA-3771	0.1
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Date	星期四, 六月 08, 2006	Sheet 18 of 48

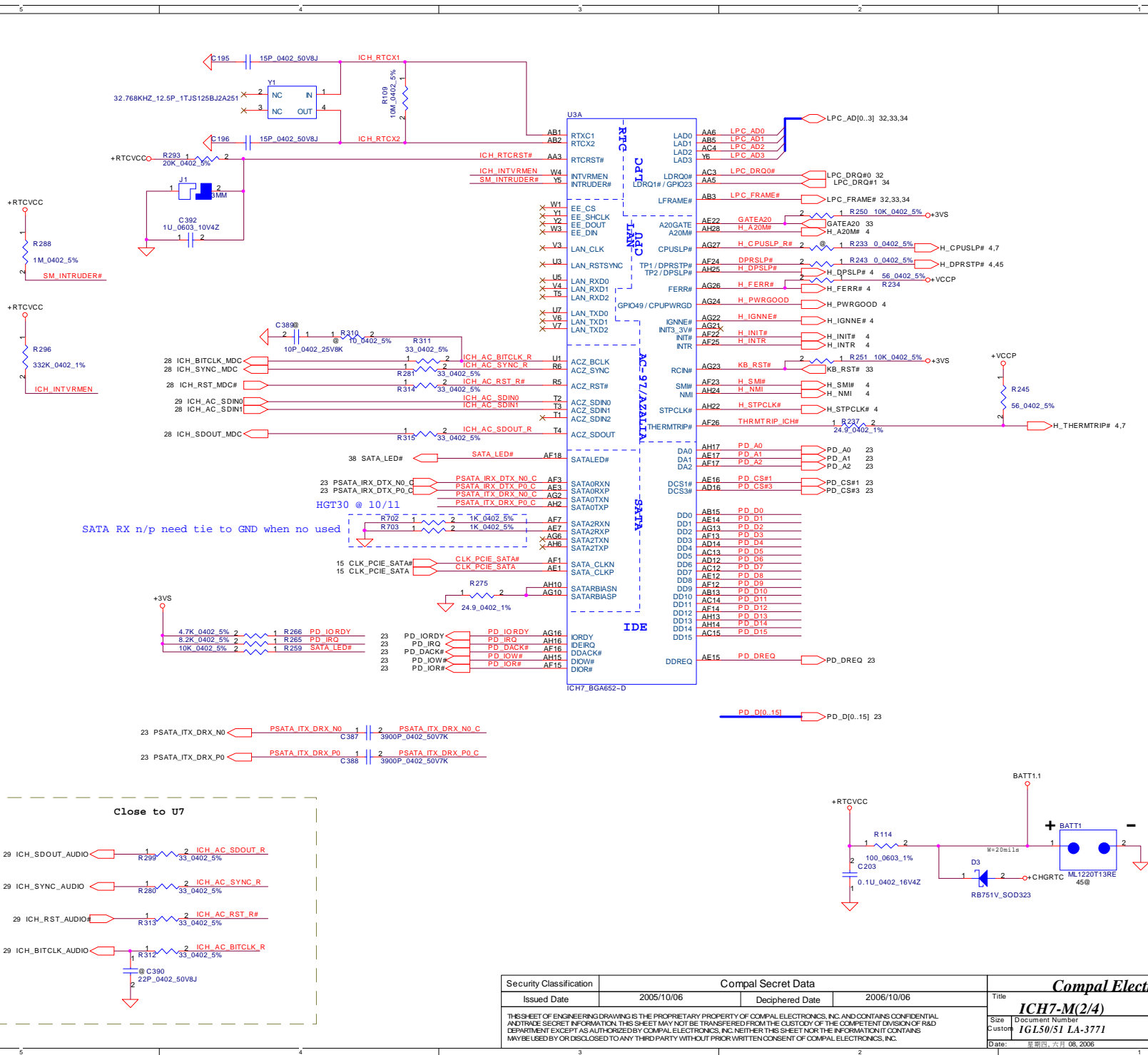
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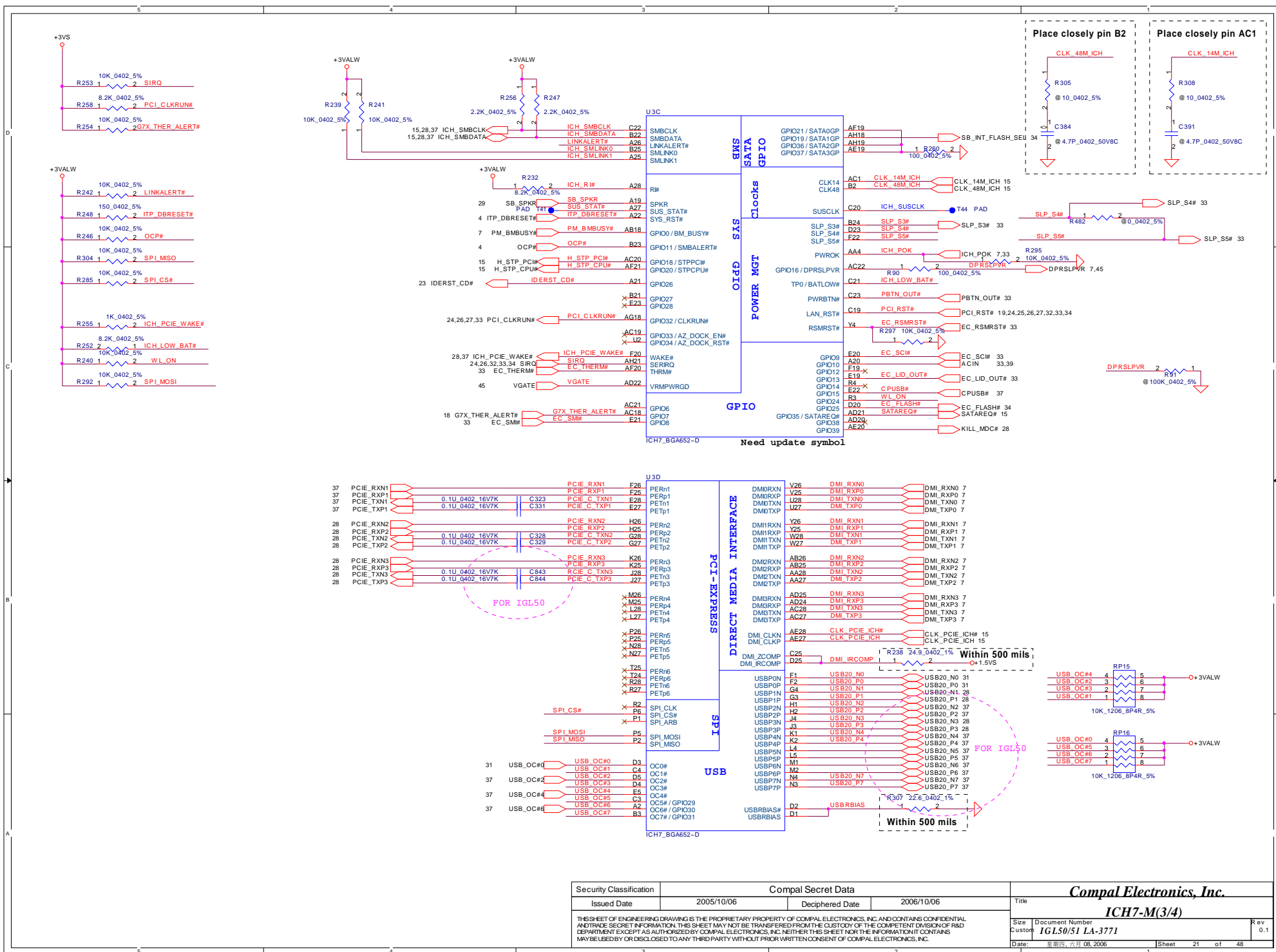
24,26,27,32 PCI_AD[0..31]



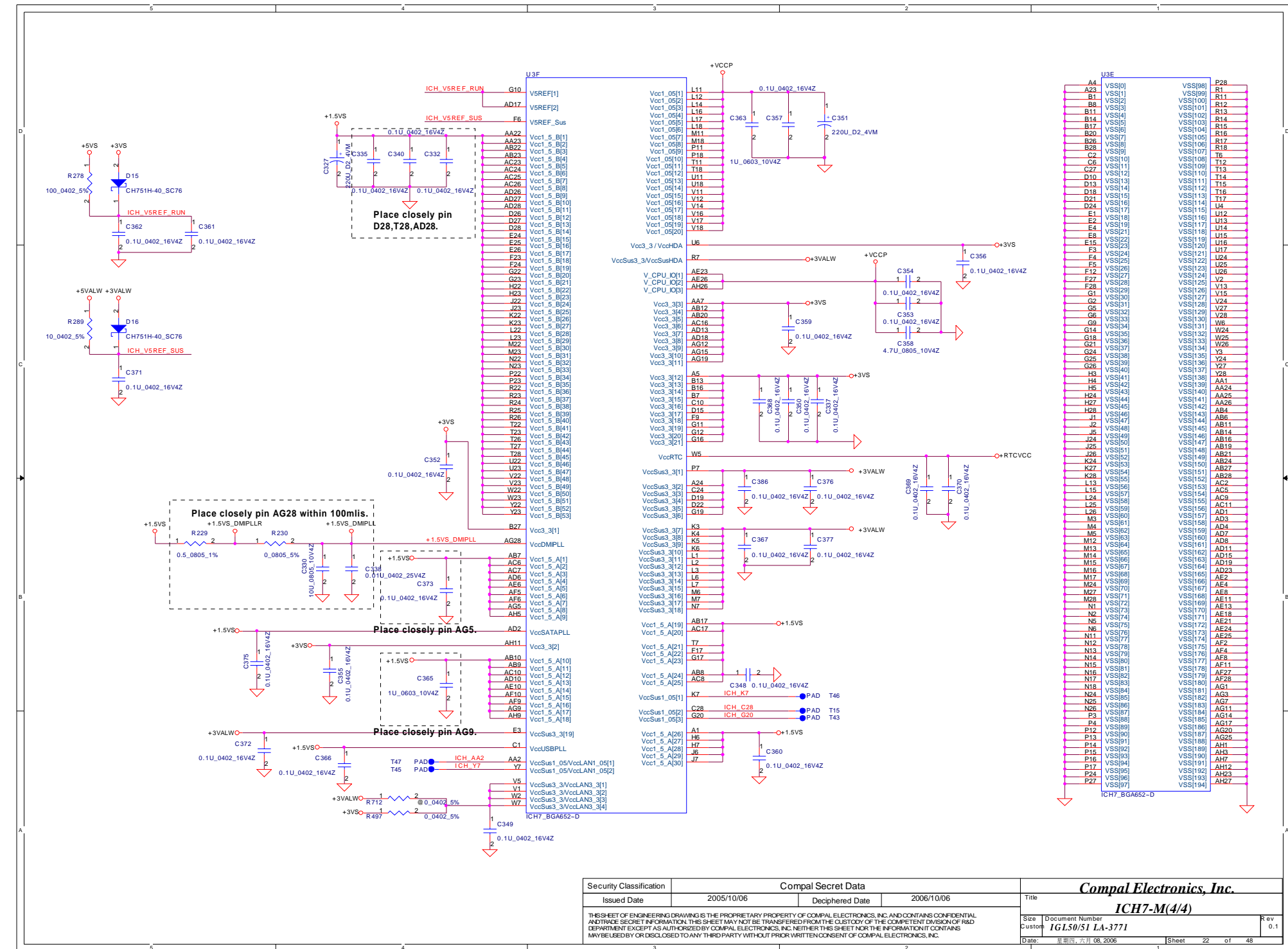
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				2006.08.08	0.1
				Sheet	19 of 48

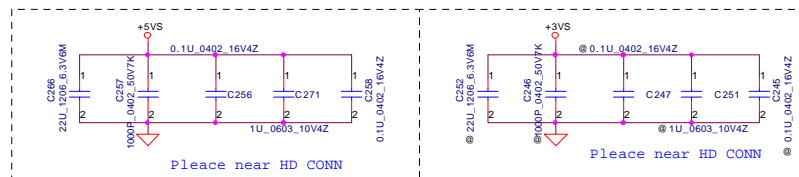
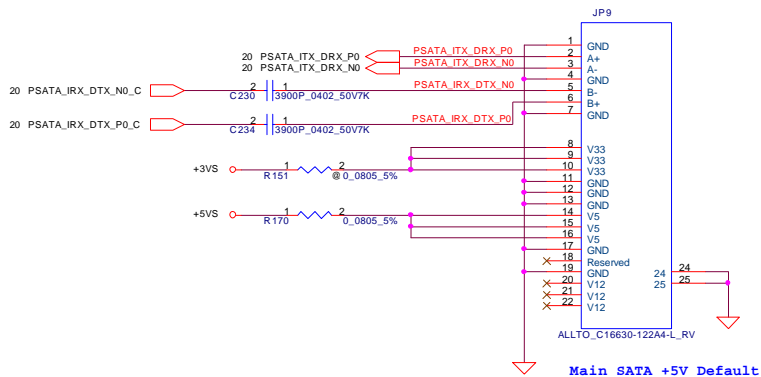


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				Date	Rev
				星期日, 六月 08, 2006	0.1
				Sheet	20 of 48

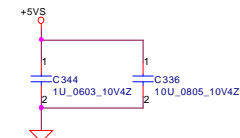
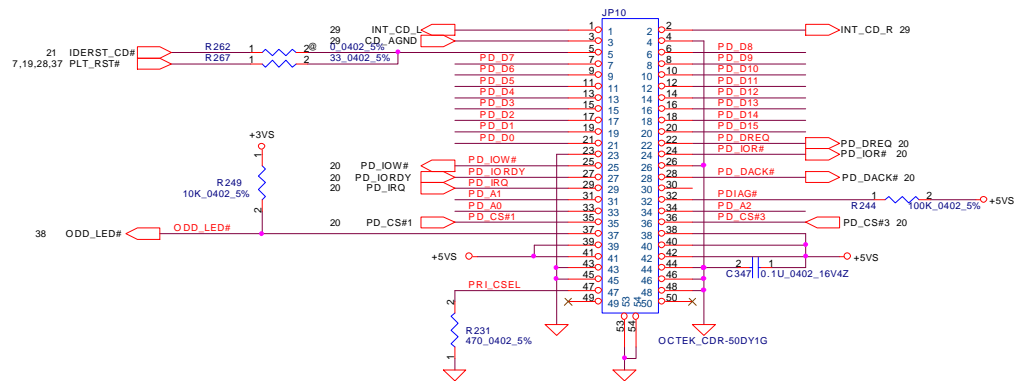


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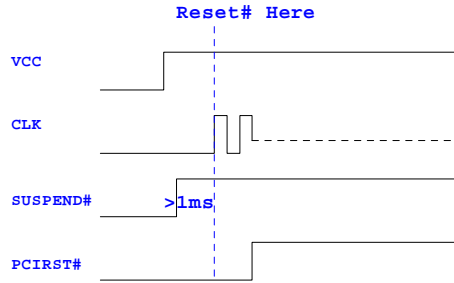


PD_D[0..15] PD_D[0..15] 20
PD_A[0..2] PD_A[0..2] 20

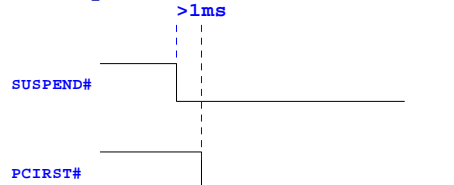


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					Rev 0.1
					Date: 星期日, 六月 08, 2006 Sheet 23 of 48

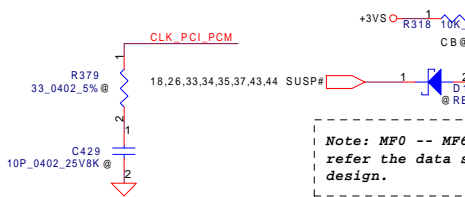
Power on RESET#



Entry S3



SUSPEND# will gate the PCIRST# or GRST#, so need S3 wake up function, SUSPEND# must be LOW ahead the PCIRST# about 1ms.



CARD_S1_A[0..25] CARD_S1_A[0..25] 25
CARD_S1_D[0..15] CARD_S1_D[0..15] 25
PCI_AD[0..31] PCI_AD[0..31] 19,26,27,32

C400 4.7U_0805_10V4Z
C402 0.1U_0402_16V4Z
C456 0.1U_0402_16V4Z
C438 0.1U_0402_16V4Z
C455 0.1U_0402_16V4Z
C404 0.1U_0402_16V4Z
C421 0.1U_0402_16V4Z

VPPD0
VPPD1
VCCD0#
VCCD1#

VCCD1#
VCCD0#
VPPD0
VPPD1
VCCP0
VCCP1
VCCSK0
VCCSK1
VCC1
VCC2
VCC3
VCC4
VCC5
VCC6
VCC7
VCCI

PCI_AD31
PCI_AD30
PCI_AD29
PCI_AD28
PCI_AD27
PCI_AD26
PCI_AD25
PCI_AD24
PCI_AD23
PCI_AD22
PCI_AD21
PCI_AD20
PCI_AD19
PCI_AD18
PCI_AD17
PCI_AD16
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PCI_AD13
PCI_AD12
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PCI_AD9
PCI_AD8
PCI_AD7
PCI_AD6
PCI_AD5
PCI_AD4
PCI_AD3
PCI_AD2
PCI_AD1
PCI_AD0

PCI_CBE#3
PCI_CBE#2
PCI_CBE#1
PCI_CBE#0

PCI_RST#
RST#
FRAME#
RDY#
TRDY#
DEVSEL#
STOP#
PERR#
SERR#
PAR
REQ#
GNT#
CLK

CB_PME#
PCI_AD20
PCI_PIRQA#
SIRQ
PCI_CLKRUN#
PCI_RST#

CB_PME#
PCI_AD20
PCI_PIRQA#
SIRQ
PCI_CLKRUN#
PCI_RST#

CB_PME#
PCI_AD20
PCI_PIRQA#
SIRQ
PCI_CLKRUN#
PCI_RST#

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PCI_RST#

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PCI_RST#

CB_PME#
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PCI_RST#

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CB_PME#
PCI_AD20
PCI_PIRQA#
SIRQ
PCI_CLKRUN#
PCI_RST#

CB_PME#
PCI_AD20
PCI_PIRQA#
SIRQ
PCI_CLKRUN#
PCI_RST#

PQFP 144
22.2 X 22.2 X 1.60

ENE CB1410 just have one vcc plane internal, if want S3 wake-up function(PME#), then at S3 status must keep all Vcc +3V. That is different with TI 1410 and O2-Micro 6912, just keep the VCCI pin +3V, the other vcc can use +3VS.

CAD31/D10
CAD30/D9
CAD29/D1
CAD28/D8
CAD27/D0
CAD26/A0
CAD25/A1
CAD24/A2
CAD23/A3
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CAD14/A9
CAD13/IORD#
CAD12/A11
CAD11/OE#
CAD10/CE2#
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CAD2/D11
CAD1/D4
CAD0/D3

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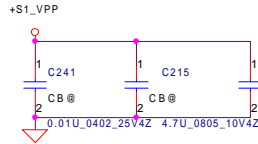
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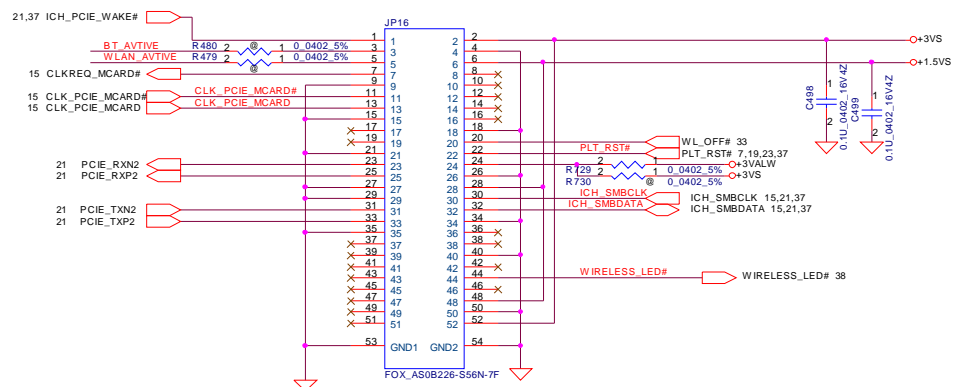
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/10/06	Deciphered Date	2006/10/06	Title	
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CUSTOMER		Document Number		IGL50/51 LA-3771	
Date: 2006/10/06		Sheet		24 of 48	

The schematic diagram illustrates the power supply section for the CP-2211 module. The central component is the U5 chip, which is connected to various external power sources and decoupling capacitors. The power sources include +5VS, +3VS, +12V, +S1_VCC, and +S1_VPP. The decoupling capacitors are C193, C194, C197, and C198 for the input supplies, and C210 and C209 for the VPP supply. The chip's output pins are connected to the PCI_RST# signal and the VCCD0, VCCD1, VPPD0, and VPPD1 pins of the CP-2211 module.

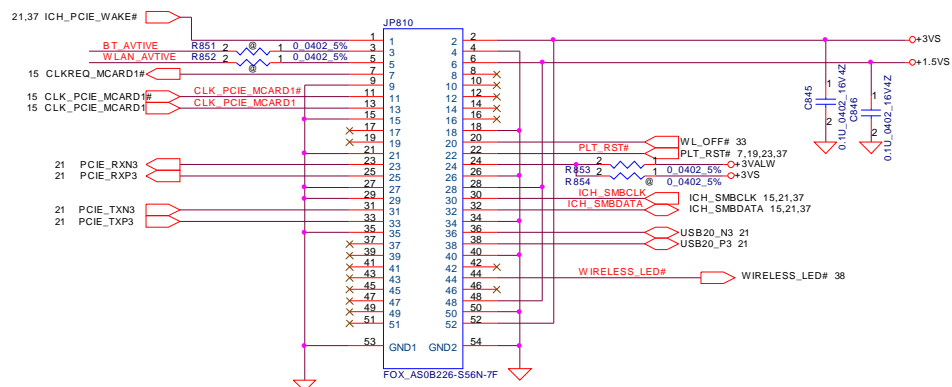


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				Size	Document Number	Rev
				Customer	IGL50/51 LA-3771 0.1	
Date:				星期四, 2006/10/06	Sheet 25 of 48	

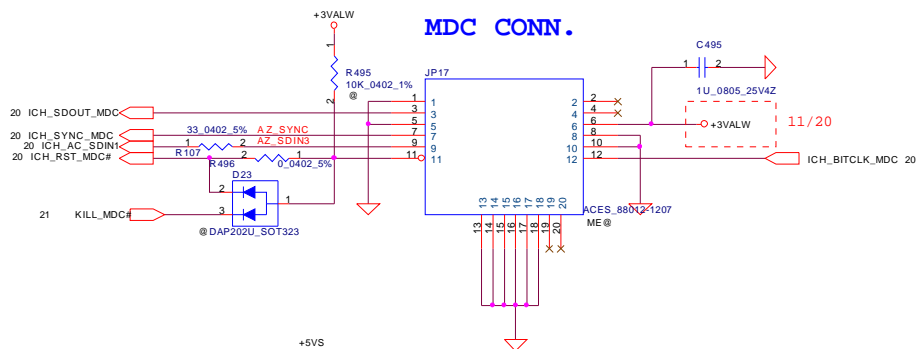
Mini-Express Card(Slot 1-WLAN)



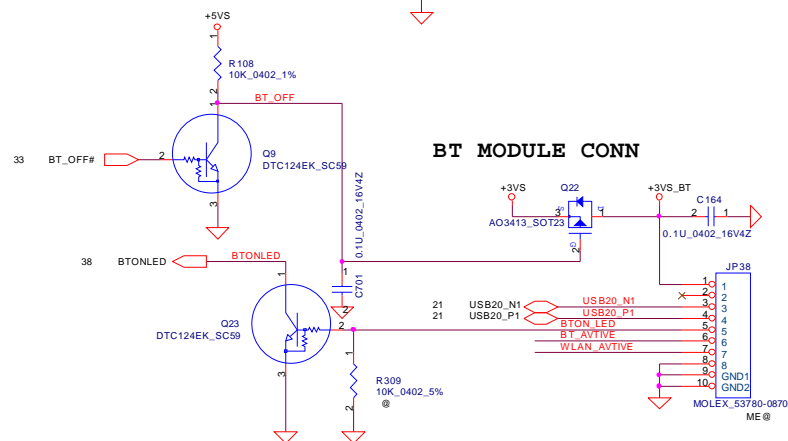
Mini-Express Card(Slot 2-WLAN)



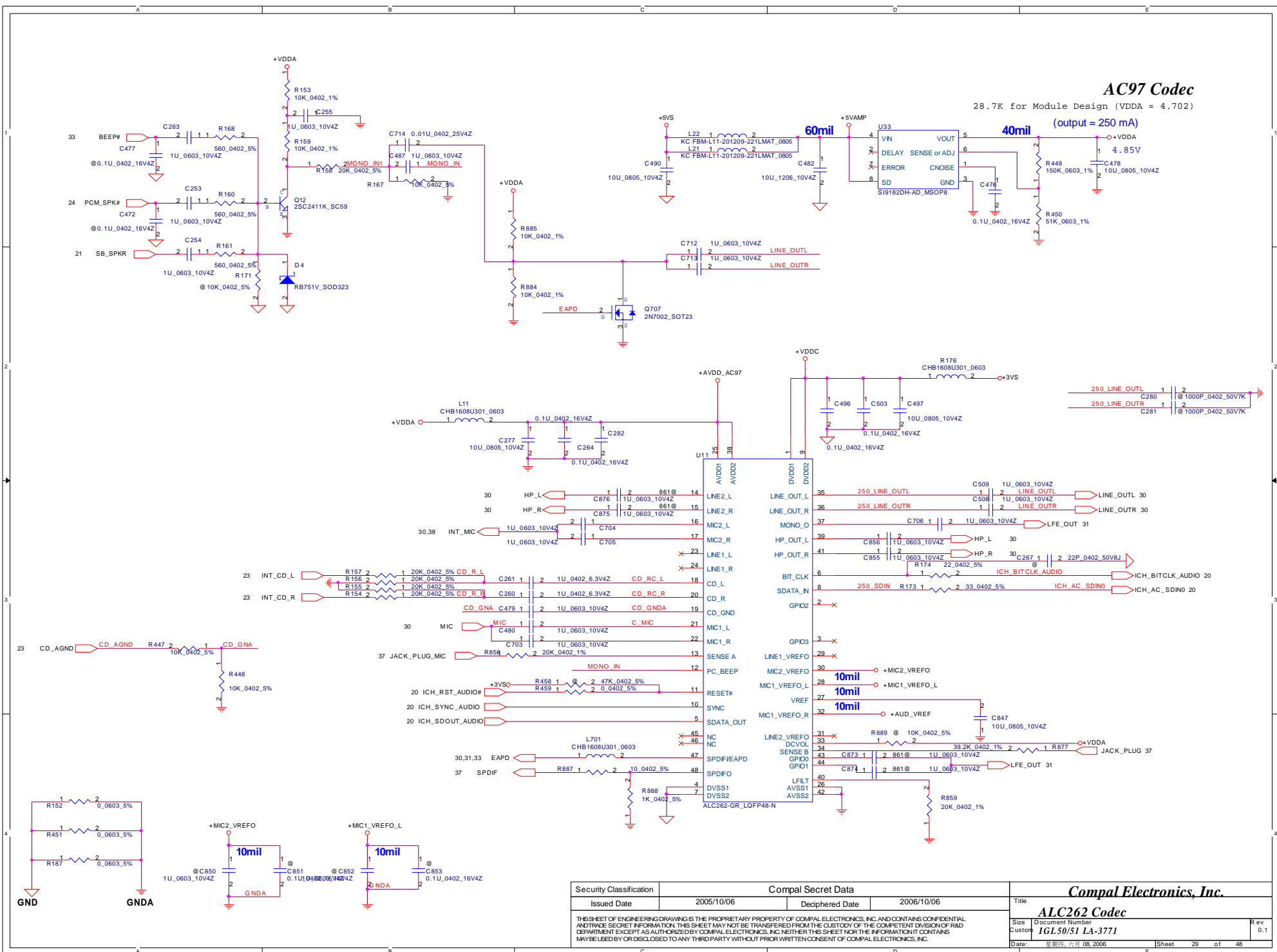
MDC CONN.

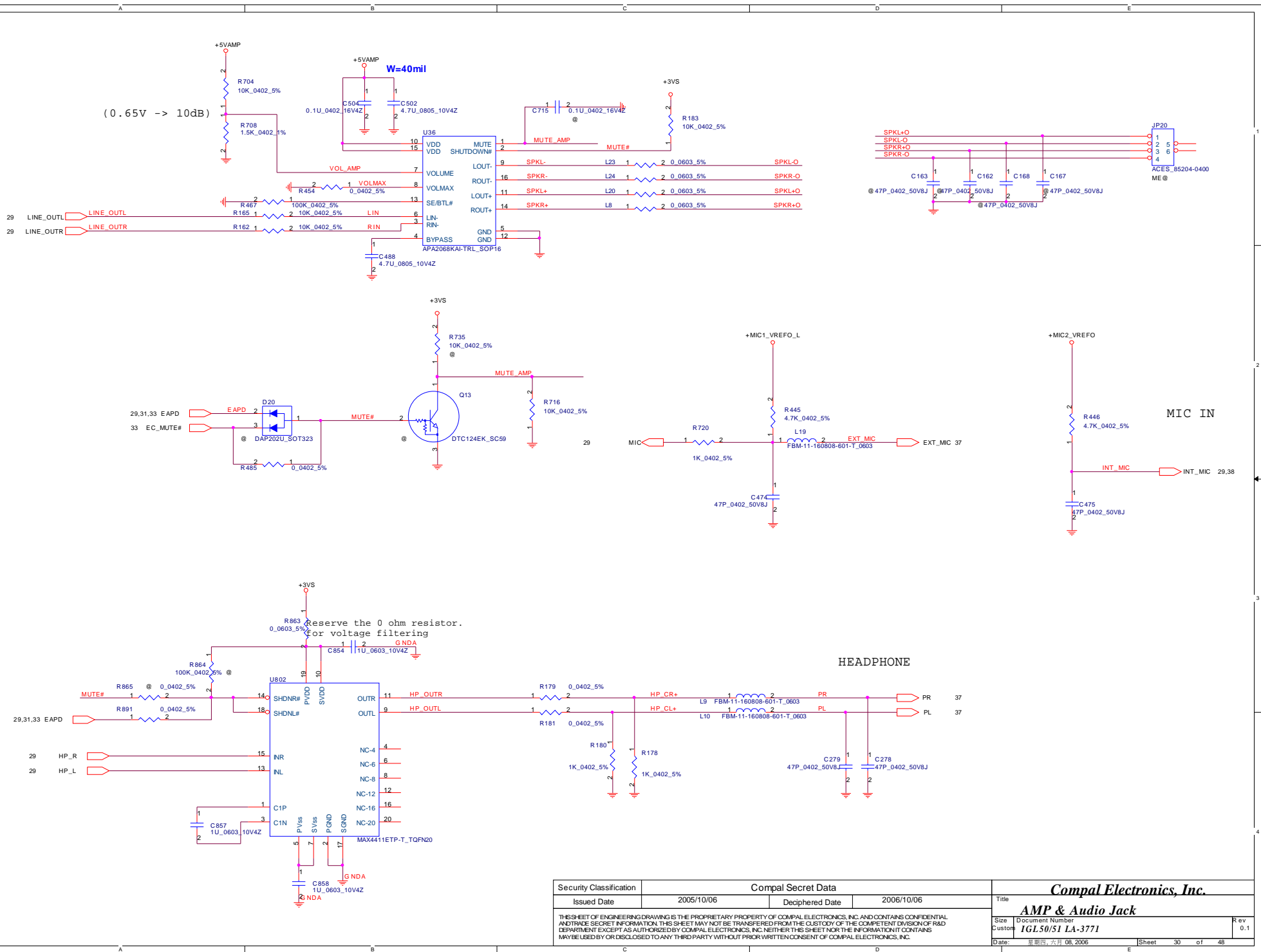


BT MODULE CONN

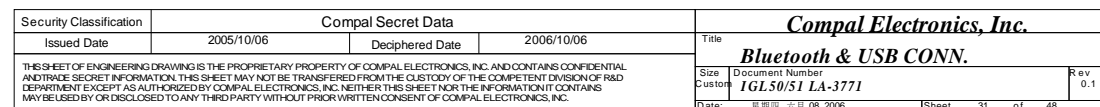


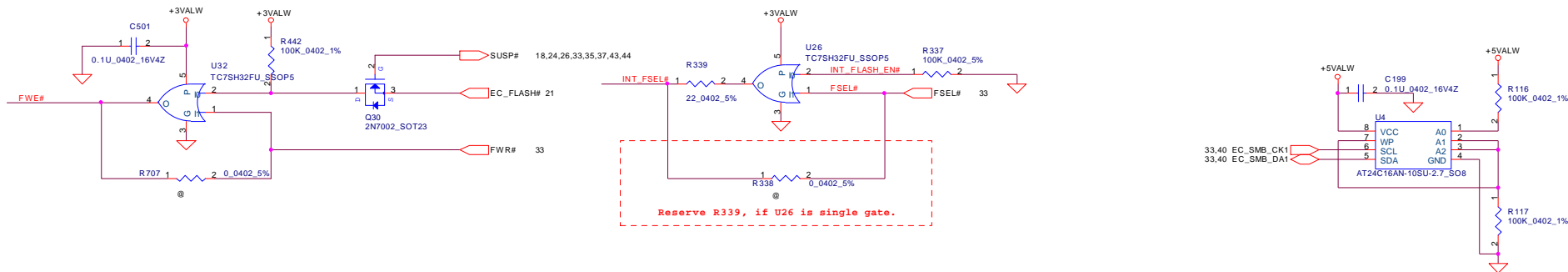
Security Classification	Compal Secret Data			Compal Electronics, Inc. Mini Card / MDC CONN		
Issued Date	2005/10/06	Deciphered Date	2006/10/06			
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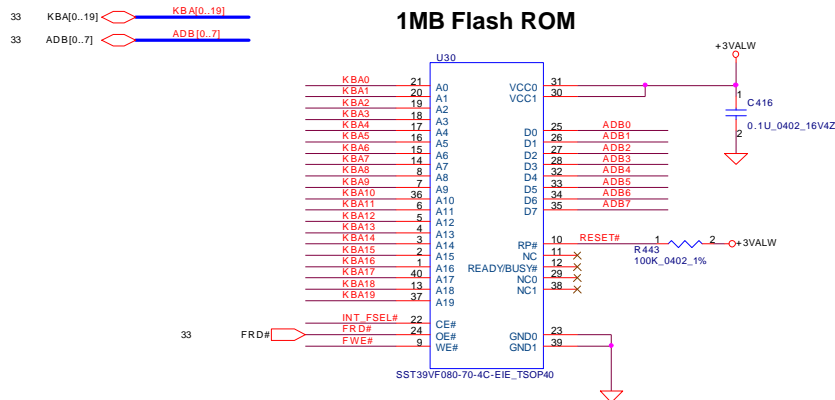


WOOFER@

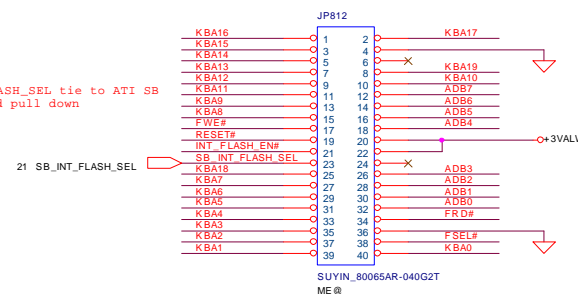




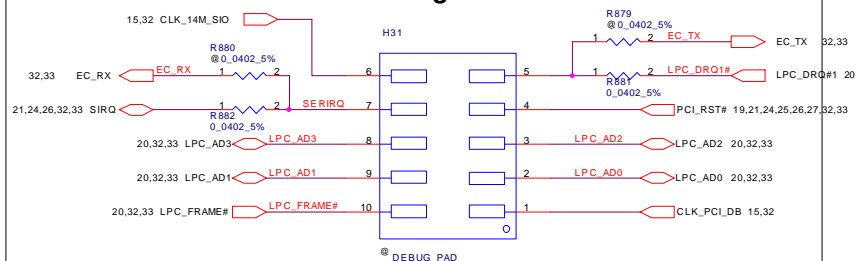
1MB Flash ROM



Change to small socket for ROM part



New LPC Debug Pad ---- MB side



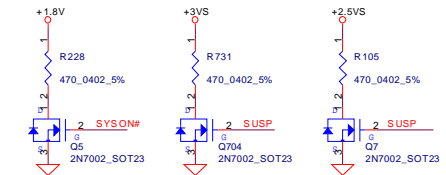
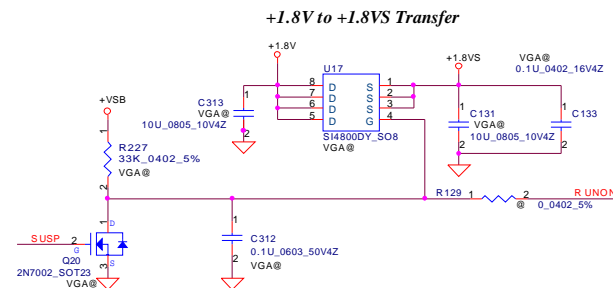
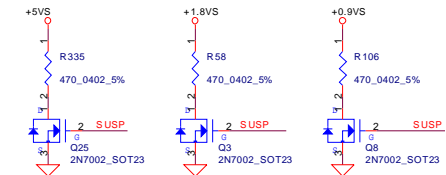
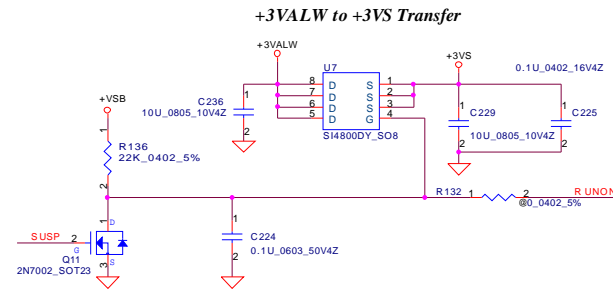
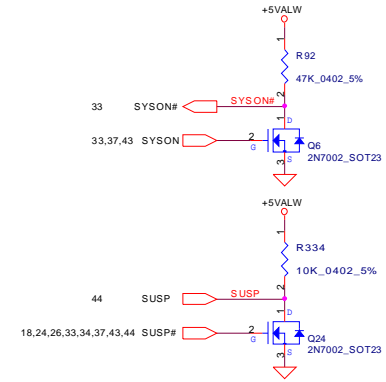
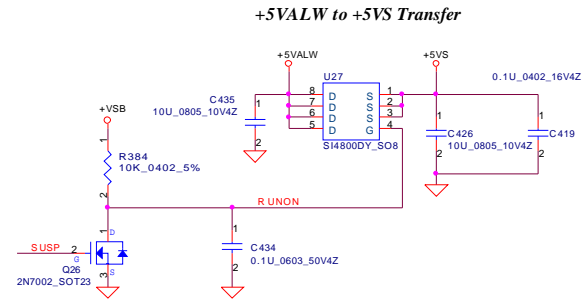
Under DDR ME Assignment Area

Keep Resistor near Debug Pad and in the same side

Standard side DIMM ---- Pin 1 near DIMM

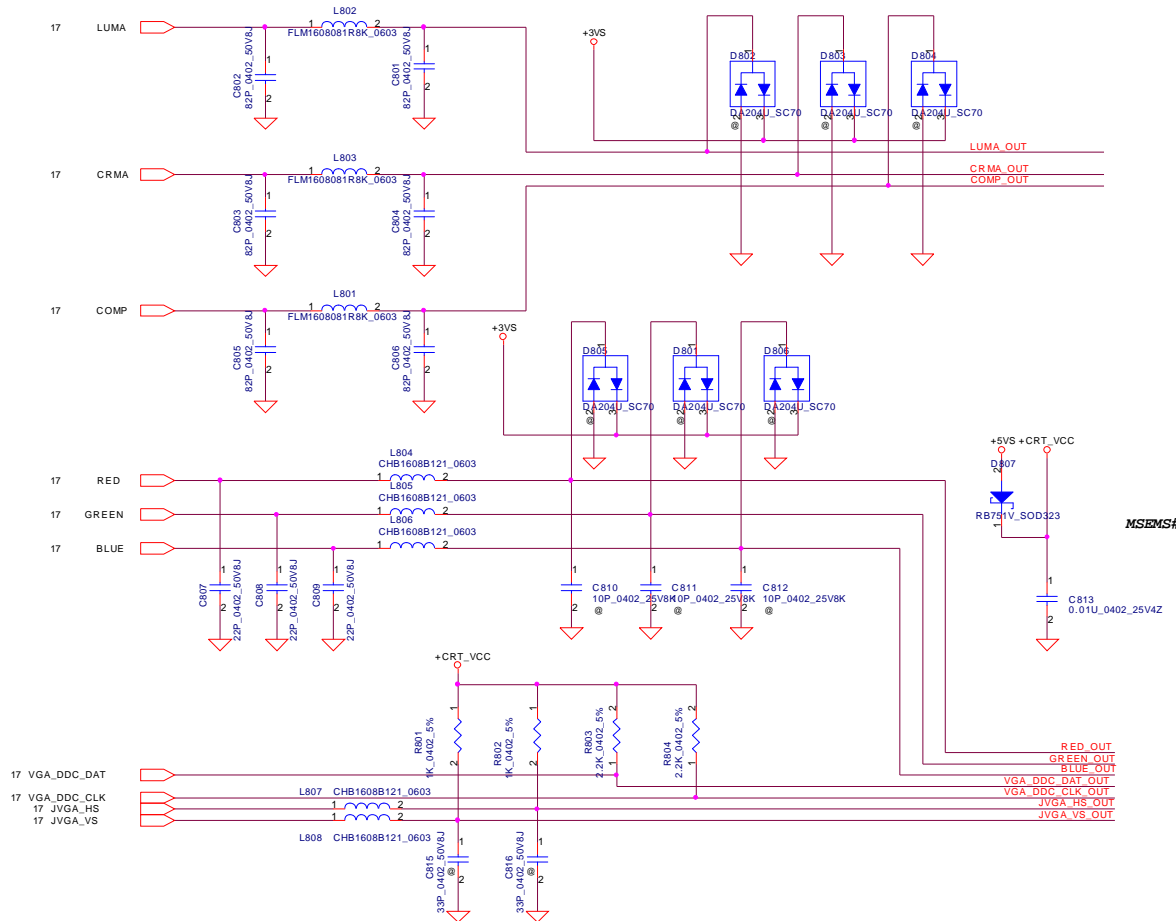
Reverse side DIMM ---- Pin 1 keep away DIMM

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Size	Custom	Document Number	1GL50/51 LA-3771	Rev 0.1
Date:	星期四, 六月 08, 2006	Sheet	34	of 48



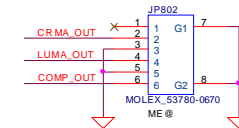
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				Document Number
				Custom
				1GL50/51 LA-3771
				Rev
				0.1
				Date
				星期四, 六月 08, 2006
				Sheet
				35 of 48

CLOSE TO JTVOUT1

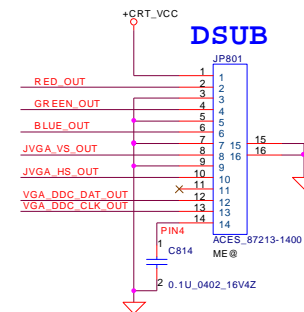


VGA I/O PORT Connector

S-VIDEO



DSUB



PIN ASSIGMENT

PIN	D-SUB	FUNCTION	PIN	SVIDEO	FUNCTION
1	9	+CART_VCC	1	1	NC
2	1	RED	2	4	CRMA
3	6	GND	3	2	GND
4	2	GREEN	4	3	LUMA
5	7	GND	5	5	GND
6	3	BLUE	6	6	CVBS
7	8	GND			
8	14	VSYNC			
9	10	GND			
10	13	HSYNC			
11	11	SENSE			
12	12	SM_DAT			
13	15	SM_CLK			
14	4	PIN4			

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				Custom	IGL50/51 LA-3771
				Date	Rev
				2006.06.08	0.1
				Sheet	36 of 48

4 in 1 Card Reader

JP814

+VCC_4IN1_XD

SD-VCC

MS-VCC

SD-CLK

SD-DAT0

SD-DAT1

SD-DAT2

SD-DAT3

SD-CMD

SD-CD-SW

SD-CD-COM

SD-WP-SW

SD-WP-COM

MS-SCLK

MS-DATA0

MS-DATA1

MS-DATA2

MS-DATA3

MS-NS

MS-BS

SD-GND

MS-GND

MS-GND

MS-GND

SHIELD GND

SHIELD GND

TATW_R012-210-LR

UMA LCD/PANEL Conn.

(60 MIL)

JP815

+VCC_4IN1_XD

LVDSAC+

LVDSAC-

LVDSB2+

LVDSB2-

LVDSB1+

LVDSB1-

LVDSB0+

LVDSB0-

EDID_CLK_LCD

EDID_DAT_LCD

LVDSB8C+

LVDSB8C-

LVDSB8B+

LVDSB8B-

LVDSB8A+

LVDSB8A-

LVDSB8+

LVDSB8-

LVDSB8C+

LVDSB8C-

LVDSB8B+

LVDSB8B-

LVDSB8A+

LVDSB8A-

LVDSB8+

LVDSB8-

ME@ ACES_87216-30006

[illegible]

RJ11+RJ45 CONN

27 MDO0+ RJ TIP MDO0+ 1 TX1+ JP807

27 MDO0- RJ TIP MDO0- 2 TX1- JP807

27 MDO1+ RJ TIP MDO1+ 3 RX1+ JP807

27 MDO2+ RJ TIP MDO2+ 4 TX2+ JP807

27 MDO2- RJ TIP MDO2- 5 TX2- JP807

27 MDO1- RJ TIP MDO1- 6 RX1- JP807

27 MDO3+ RJ TIP MDO3+ 7 RX2+ JP807

27 MDO3- RJ TIP MDO3- 8 RX2- JP807

RJ11_1 RJ11

RJ11_2 RJ11

RJ45_PR RJ45

ALLTO_C100B6-110A4-L 0.1U_0402_16V4Z

ME 1 2 C830 1000P_1206_2KV7K C834 1 2 4.7U_0805_10V4Z

R827 2 1 9 RJ11_1

RJ TIP 2 2 0.0603_5% 10 RJ11_2

RJ RING 2 2 0.0603_5%

R828

SGND1 11

SGND2 12

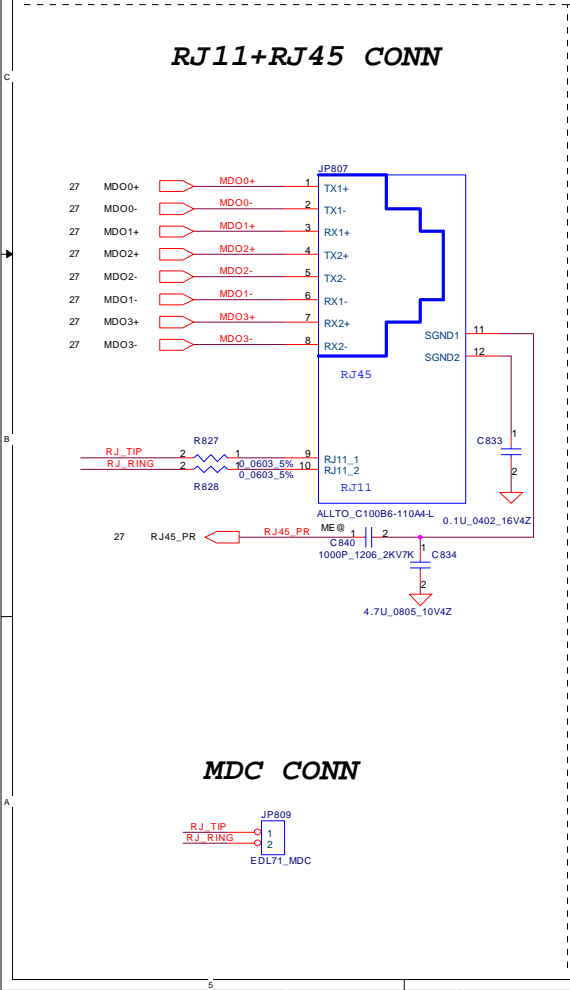
C833 1 2

MDC CONN

RJ TIP JP809

RJ RING 1 2

EDL71_MDC



RJ11+RJ45 CONN

27 MDO0+ RJ TIP MDO0+ 1 TX1+ JP807

27 MDO0- RJ TIP MDO0- 2 TX1-

27 MDO1+ RJ TIP MDO1+ 3 RX1+

27 MDO2+ RJ TIP MDO2+ 4 TX2+

27 MDO2- RJ TIP MDO2- 5 TX2-

27 MDO1- RJ TIP MDO1- 6 RX1-

27 MDO3+ RJ TIP MDO3+ 7 RX2+

27 MDO3- RJ TIP MDO3- 8 RX2-

RJ11_1 RJ11

RJ11_2

ALLTO_C100B6-110A4-L 0.1U_0402_16V4Z

ME @ 1 2

1000P_1206_2KV7K C834

4.7U_0805_10V4Z

RJ45_PR RJ45

R827 R828

RJ TIP RJ RING

2 2

1 0.0603_5% 10 0.0603_5%

C833

1 2

SGND1 SGND2

11 12

1 2

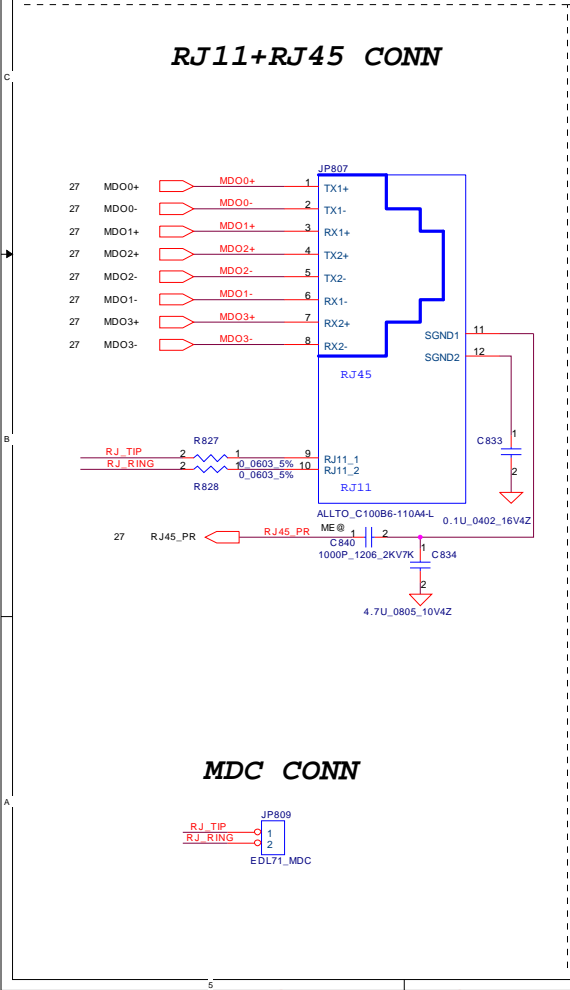
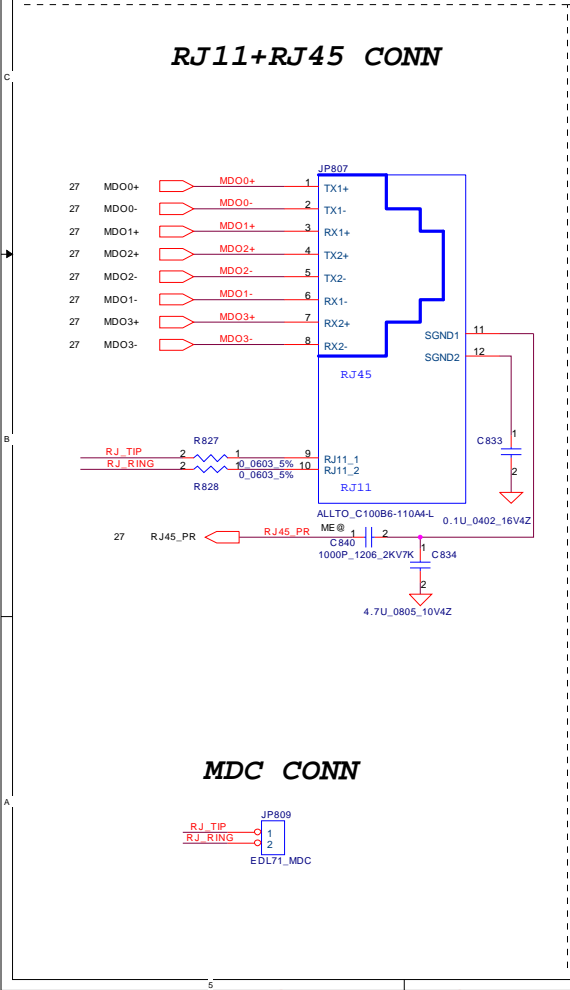
MDC CONN

JP809

RJ TIP RJ RING

1 2

EDL71_MDC

[illegible][illegible][illegible]

RJ11+RJ45 CONN

MDC CONN

LED Indicator

Wireless / Bluetooth LED Blue

BATT_CHG_LED# Blue

BATT_LOW_LED# Blue

STATUS	
AC	BLUE
Chargin	Blinking Blue
Low BATT	Amber

Function		
KEY Matrix	K016	K017
KS10	DW-UP	DW-DOWN
KS11	DW-ENTER	MUTE

Dial Wheel

NOVO BTN

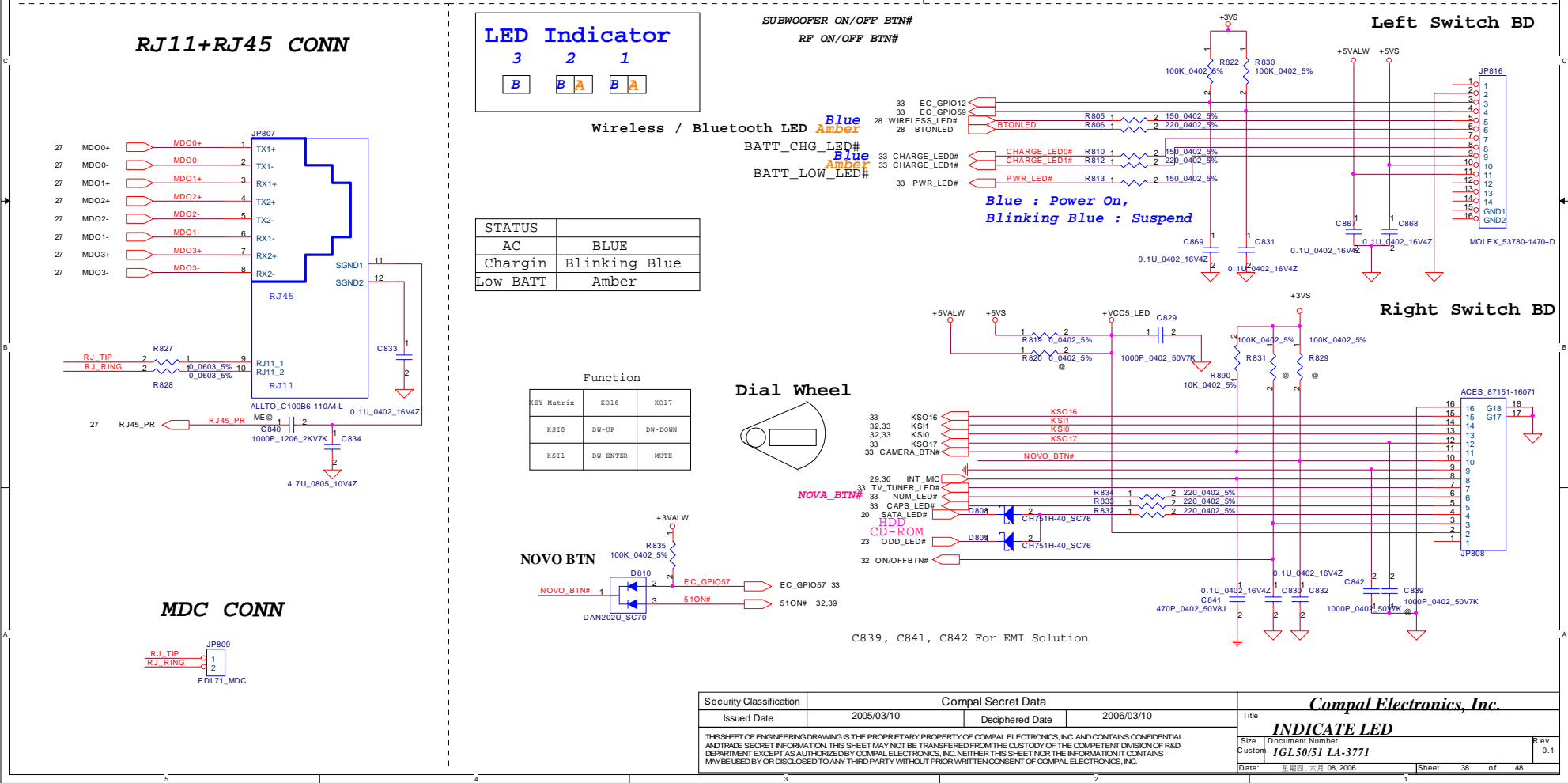
SUBWOOFER_ON/OFF_BTN#

RF_ON/OFF_BTN#

Right Switch BD

C839, C841, C842 For EMI Solution

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				Custom	1GL50/S1 LA-3771
				Date	星期五, 六月 08, 2006
				Sheet	38 of 48
				Rev	0.1



RJ11+RJ45 CONN

MDC CONN

LED Indicator

Wireless / Bluetooth LED Blue
BATT_CHG_LED# Blue
BATT_LOW_LED# Amber

STATUS	
AC	BLUE
Chargin	Blinking Blue
Low BATT	Amber

Function	K016	K017
KEY Matrix		
KS10	DN-UP	DN-DOWN
KS11	DN-ENTER	MUTE

Dial Wheel

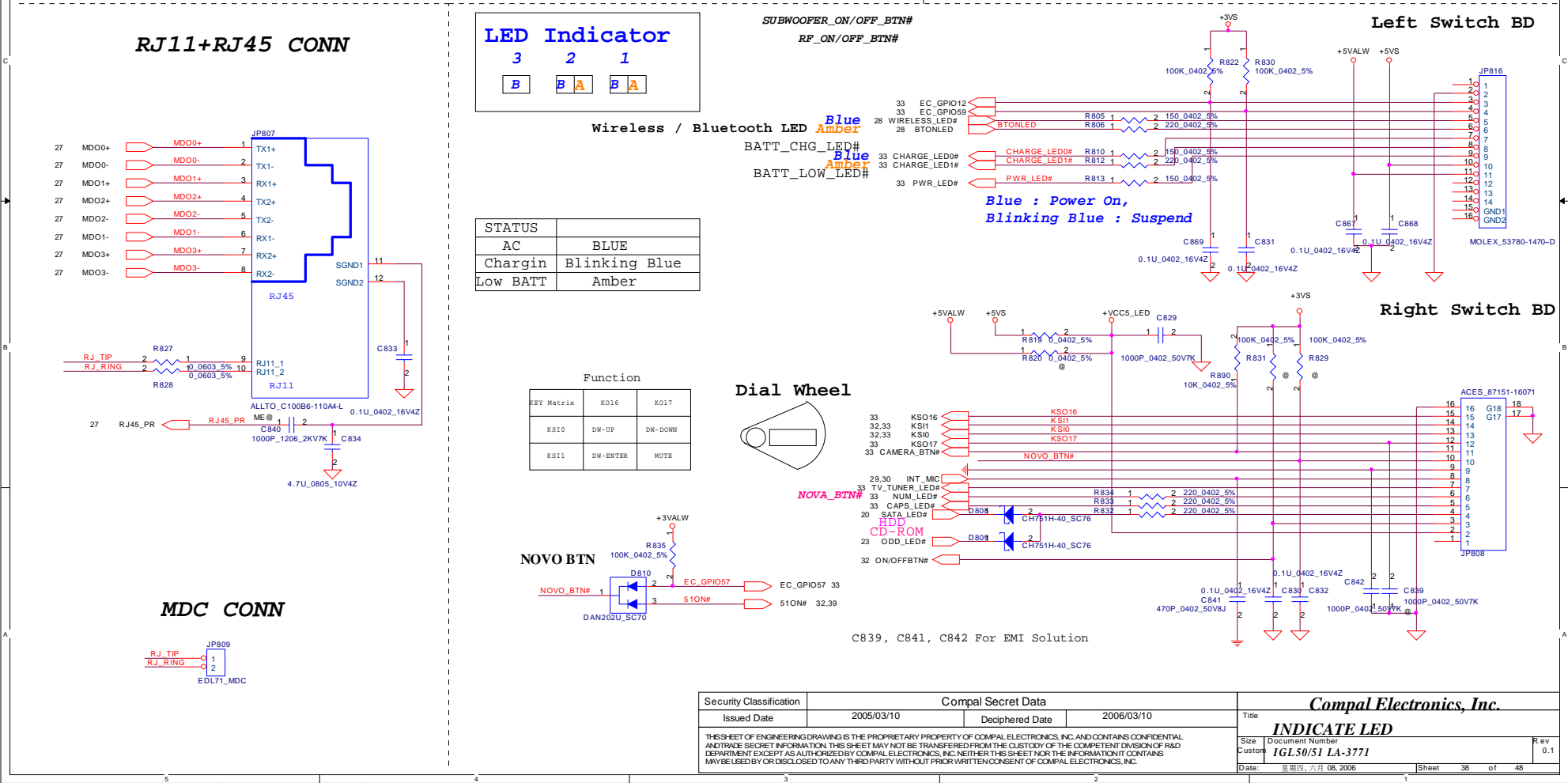
SUBWOOFER_ON/OFF_BTN#

RF_ON/OFF_BTN#

Blue : Power On,
Blinking Blue : Suspend

C839, C841, C842 For EMI Solution

Security Classification				Compal Secret Data				Compal Electronics, Inc.			
Issued Date				2005/03/10				Title			
				Deciphered Date				INDICATE LED			
				2006/03/10				Document Number			
								1GL50/51 LA-3771			
								Rev			
								0.1			
								Date			
								2006/03/10			
								Sheet			
								38 of 48			



RJ11+RJ45 CONN

MDC CONN

LED Indicator

Wireless / Bluetooth LED Blue
BATT_CHG_LED# Blue
BATT_LOW_LED# Blue

STATUS	
AC	BLUE
Chargin	Blinking Blue
Low BATT	Amber

Function		
KEY Matrix	K016	K017
KS10	DW-UP	DW-DOWN
KS11	DW-ENTER	MUTE

Dial Wheel

SUBWOOFER_ON/OFF_BTN#

RF_ON/OFF_BTN#

Blue : Power On,
Blinking Blue : Suspend

C839, C841, C842 For EMI Solution

Left Switch BD

Right Switch BD

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				Custom	1GL50/51 LA-3771	0.1
				Date	星期五, 六月 08, 2006	Sheet 38 of 48

RJ11+RJ45 CONN

MDC CONN

Wireless / Bluetooth LED Blue
BATT_CHG_LED# Blue
BATT_LOW_LED# Amber

STATUS	
AC	BLUE
Chargin	Blinking Blue
Low BATT	Amber

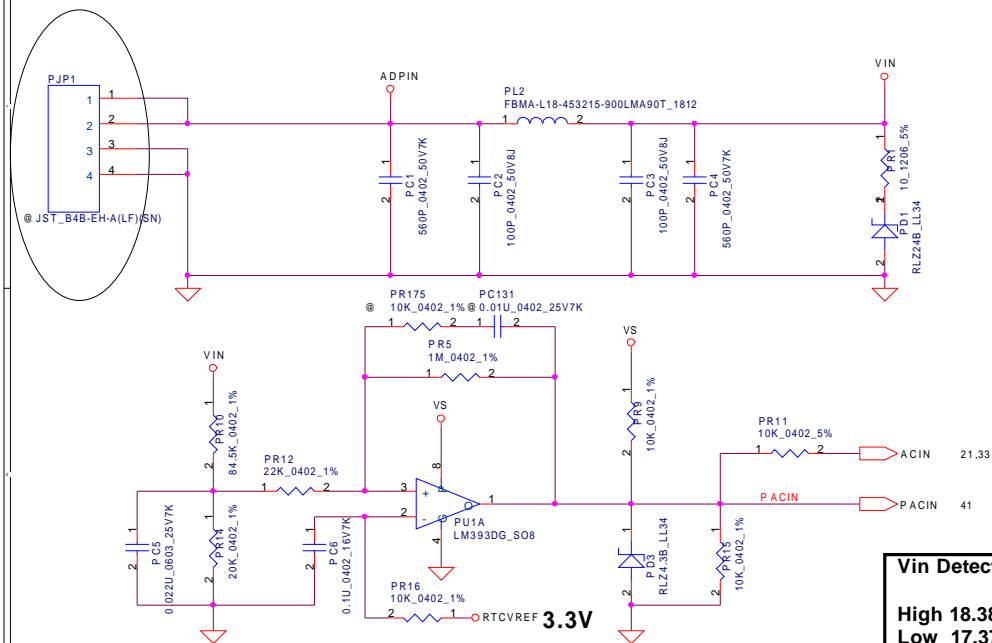
Function		
KEY Matrix	K016	K017
KS10	DW-UP	DW-DOWN
KS11	DW-ENTER	MUTE

SUBWOOFER_ON/OFF_BTN#
RF_ON/OFF_BTN#

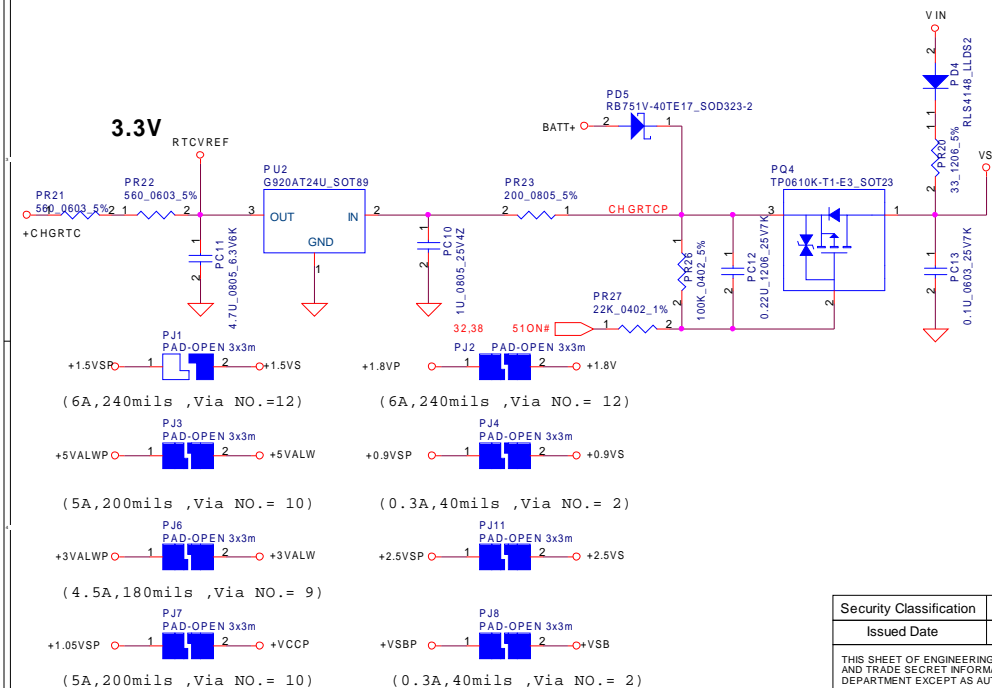
Blue : Power On,
Blinking Blue : Suspend

C839, C841, C842 For EMI Solution

Security Classification				Compal Secret Data				Compal Electronics, Inc.			
Issued Date				2005/03/10				Title			
				Deciphered Date				INDICATE LED			
				2006/03/10				Document Number			
								1GL50/51 LA-3771			
								Rev			
								0.1			
								Date			
								2006/03/10			
								Sheet			
								38 of 48			

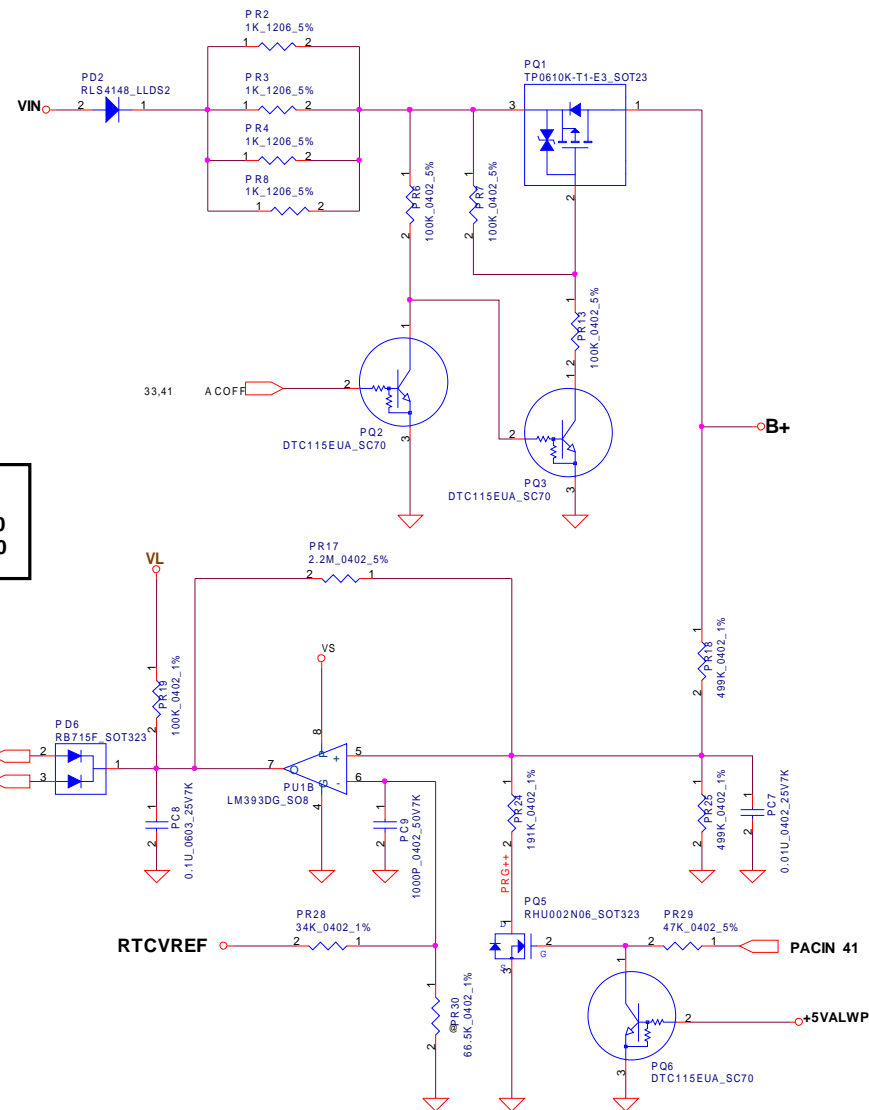


Vin Detector			
High	18.384	17.901	17.430
Low	17.370	16.907	16.630

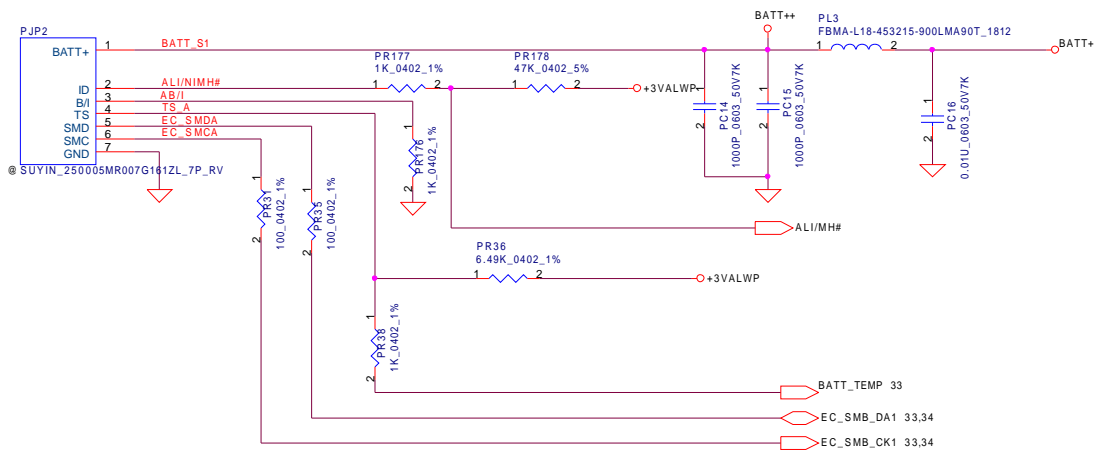


ACIN Precharge detector			
	Min.	typ.	Max.
H-->L	14.620V	14.853V	15.245V
L-->H	15.534V	15.970V	16.421V

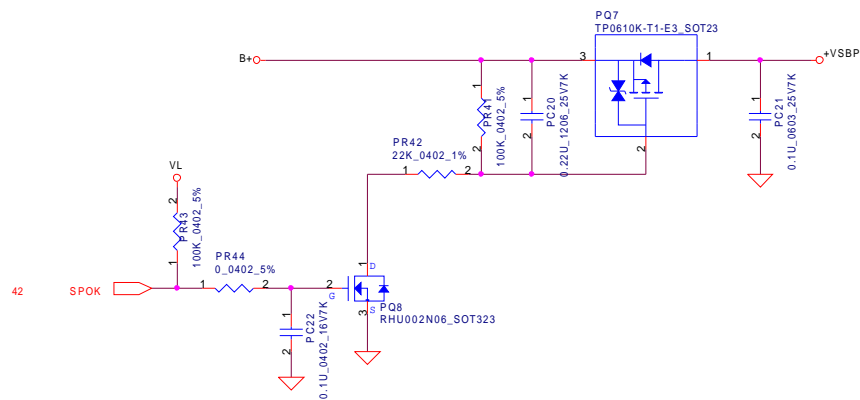
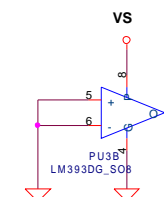
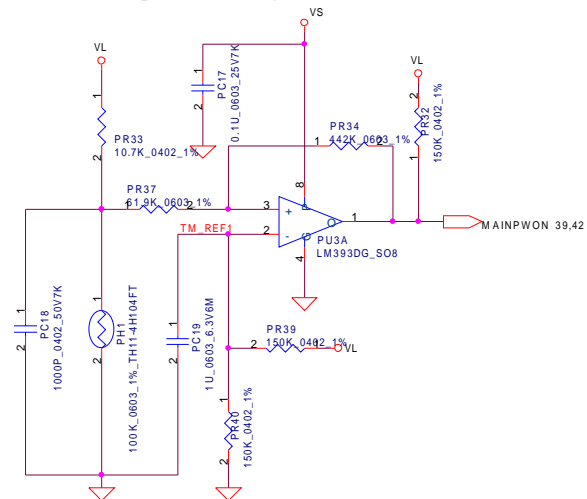
BATT ONLY Precharge detector			
	Min.	typ.	Max.
H-->L	6.169V	6.231V	6.361V
L-->H	7.168V	7.349V	7.537V



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Size B	Document Number			Rev 0.1	
Date: 星期四, 六月 08, 2006		Sheet 39 of 48			

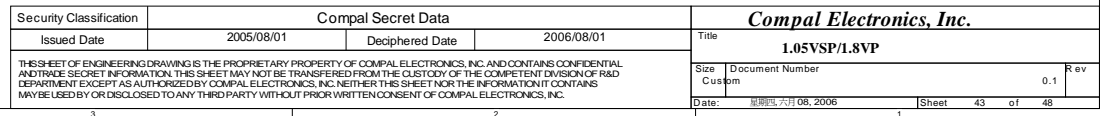


PH1 under CPU botten side :
CPU thermal protection at 85 degree C
Recovery at 70 degree C

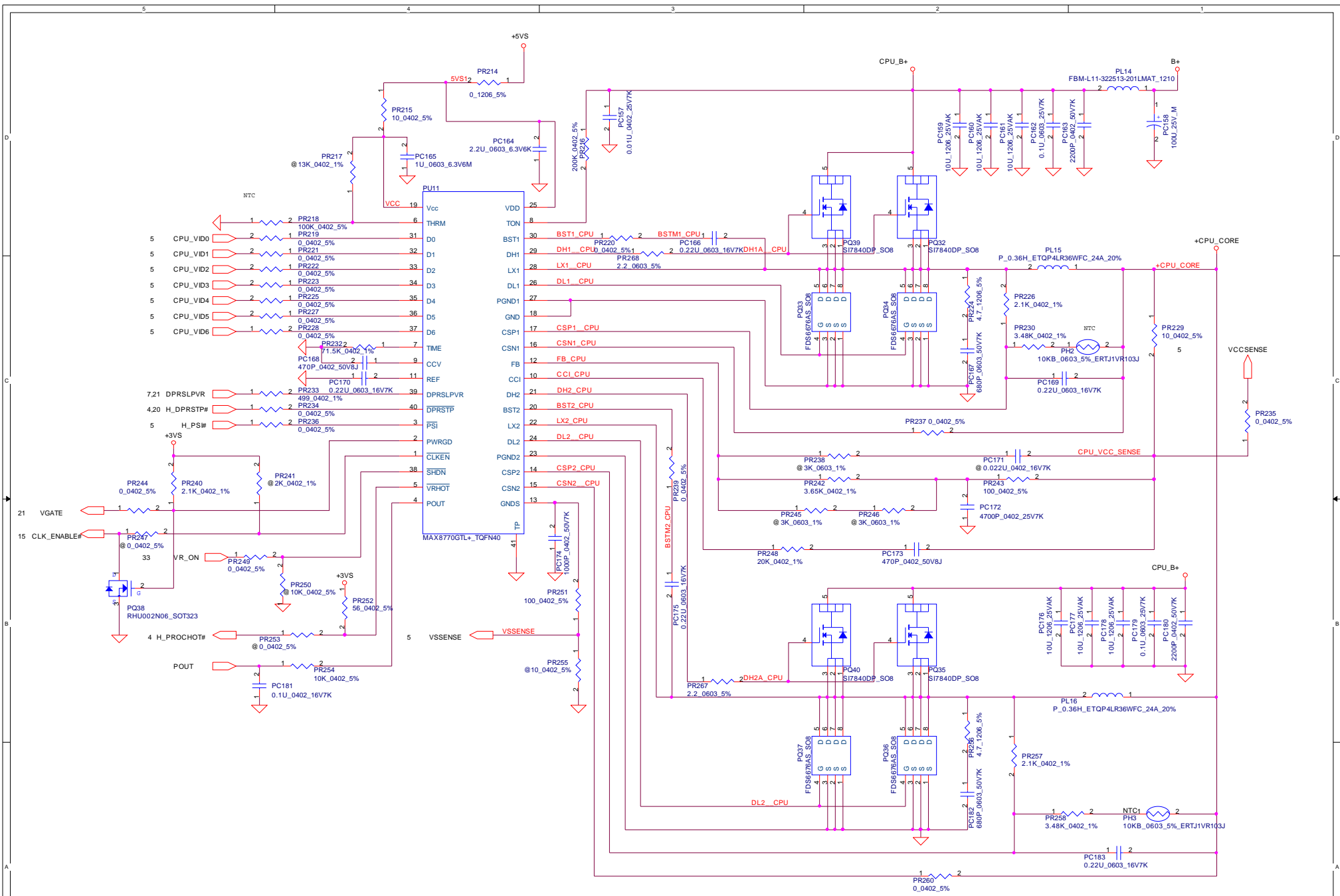


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Date: 星期四, 六月 08, 2006		Sheet 40 of 48			

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Size	Document Number	Date: 星期四, 六月 08, 2006		Sheet	45 of 48
Cuspm	Rev	0.1			

Version change list (P.I.R. List)

Page 1 of 1
for PWR

Item	Fixed Issue	Reason for change	Rev.	PG#	Modify List	VER	Phase
1							
2							
3							
4							
5							
6							
7							
8							
9							
10							
11							
8							
9							

Compal Electronics, Inc.			
Title PIR (PWR)			
Size	Document Number		Rev 0.2
Date: 星期四, 六月 08, 2006	Sheet 46	of 48	

Version change list (P.I.R. List)

Item	Fixed Issue	Rev.	PG#	Modify List	B.Ver#	Phase
1					0.2	DVT
2					0.2	DVT
3					0.2	DVT
4					0.2	DVT
5					0.2	DVT
6					0.2	DVT
7					0.2	DVT
8					0.2	DVT
9					0.2	DVT
10					0.2	DVT
11					0.2	DVT

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Compal Electronics, Inc.			
Title			
PIR			
Size	Document Number		Rev
	HGT30/31 LA-3061		0.0
Date:	日期 1, 六月 08, 2006		Sheet 47 of 48

Version change list (P.I.R. List)

Item	Fixed Issue	Rev.	PG#	Modify List	B.Ver#	Phase
12					0.3	PVT
13					0.3	PVT
14					0.3	PVT
15					0.3	PVT
16					0.3	PVT

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Compal Electronics, Inc.		
Title		
PIR		
Size	Document Number	Rev
	HGT30/31 LA-3061	0.0
Date:	日期 1, 六月 08, 2006	Sheet 48 of 48